

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
A																		
B																		
C																		
D																		
E																		
F																		
G																		
H																		
I																		

MAINS VOLTAGE	3 x 400V 50HZ	400V Conductor Color - Black	
AUXILIARY CIRCUIT VOLTAGE	230V 50Hz	230V Conductor Color - Black	
AUXILIARY CIRCUIT VOLTAGE	24VDC	24DVC Conductor Color - Blue	
PROTECTION	IP55	Ambient Temperature -	

Folio	Title	Plants	Location	Rev	Author	Date
1	References Page					
2	List of Folios					
3	List of Folios					
4	Mains Power Supply	=EC1	-IGC	1.0	IM	01/09/2018
5	Auxiliary Power Supply	=EC1	-IGC	1.0	IM	01/09/2018
6	Emergency Stop Circuit	=EC1	-IGC	1.0	IM	01/09/2018
7	Emergency Stop Power	=EC1	-IGC	1.0	IM	01/09/2018
8	VX Gate Control Circuit	=EC1	-IGC	1.0	IM	01/09/2018
9	V1 Gate Control Circuit	=EC1	-IGC	1.0	IM	01/09/2018
10	V2 Gate Control Circuit	=EC1	-IGC	1.0	IM	01/09/2018
11	V3 Gate Control Circuit	=EC1	-IGC	1.0	IM	01/09/2018
12	V4 Gate Control Circuit	=EC1	-IGC	1.0	IM	01/09/2018
13	V5 Gate Control Circuit	=EC1	-IGC	1.0	IM	01/09/2018
14	V6 Gate Control Circuit	=EC1	-IGC	1.0	IM	01/09/2018
15	V7 Gate Control Circuit	=EC1	-IGC	1.0	IM	01/09/2018
16	A0 PLC Layout	=EC1	-IGC	1.0	IM	01/09/2018
17	A0 Input Module	=EC1	-IGC	1.0	IM	01/09/2018
18	A0 Output Module	=EC1	-IGC	1.0	IM	01/09/2018
19	A1/1 Input Module	=EC1	-IGC	1.0	IM	01/09/2018
20	A1/2 Input Module	=EC1	-IGC	1.0	IM	01/09/2018
21	A1 Output Module	=EC1	-IGC	1.0	IM	01/09/2018
22	A2/1 Input Module	=EC1	-IGC	1.0	IM	01/09/2018
23	A2/2 Input Module	=EC1	-IGC	1.0	IM	01/09/2018
24	A2 Output Module	=EC1	-IGC	1.0	IM	01/09/2018
25	A3/1 Input Module	=EC1	-IGC	1.0	IM	01/09/2018
26	A3/2 Input Module	=EC1	-IGC	1.0	IM	01/09/2018
27	A3 Output Module	=EC1	-IGC	1.0	IM	01/09/2018
28	A4/1 Input Module	=EC1	-IGC	1.0	IM	01/09/2018
29	A4/2 Input Module	=EC1	-IGC	1.0	IM	01/09/2018

Author:
Date:

List of Folios

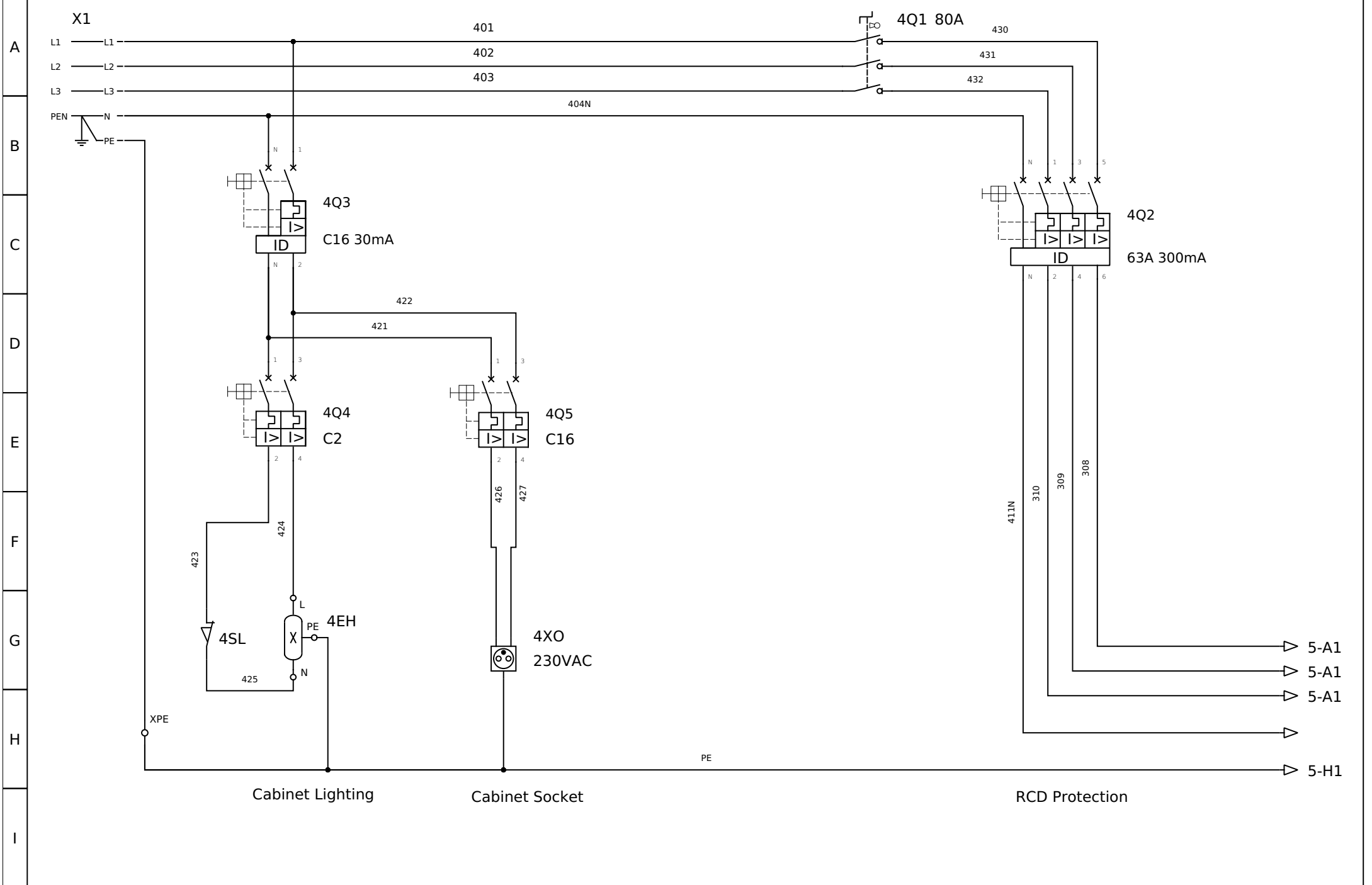
File:
Folio: 2

Folio	Title	Plants	Location	Rev	Author	Date
30	A4 Output Module	=EC1	-IGC	1.0	IM	01/09/2018
31	A5/1 Ana Input Module	=EC1	-IGC	1.0	IM	01/09/2018
32	A5/2 Ana Input Module	=EC1	-IGC	1.0	IM	01/09/2018
33	TB1 Terminal Bord	=EC1	-IGC	1.0	IM	01/09/2018
34	TB2 Terminal Bord	=EC1	-IGC	1.0	IM	01/09/2018
35	TB3 Terminal Bord	=EC1	-IGC	1.0	IM	01/09/2018
36	TB4 Terminal Bord	=EC1	-IGC	1.0	IM	01/09/2018
37	TB5 Terminal Bord	=EC1	-IGC	1.0	IM	01/09/2018
38	PB1 Panel Front View	=EC1	-IGC	1.0	IM	01/09/2018
39	PB2 Panel Front View	=EC1	-IGC	1.0	IM	02/09/2018
40	CX1 Modbus TCP	=EC1	-IGC	1.0	IM	03/09/2018

Author:
Date:

List of Folios

File:
Folio: 3



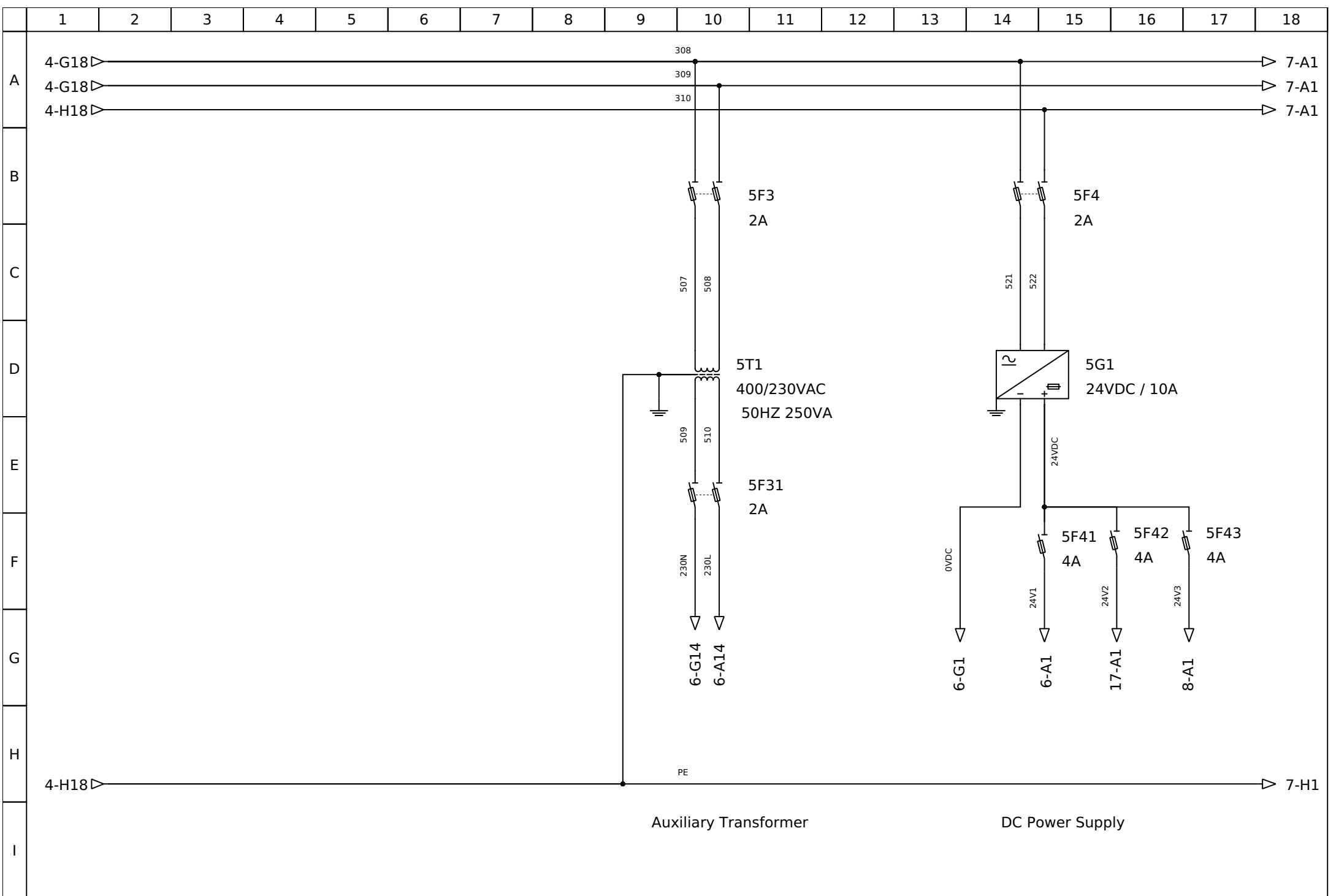
Cabinet Lighting

Cabinet Socket

RCD Protection

				DATE	01/09/2018				Example project		Proj N°:	
				DSGN	IM				=EC1		-IGC	
1	1st issue		22	CHKD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet			Page: 4	
REV	MOD	DATE	INIT	APPD	AM						Next: 5	

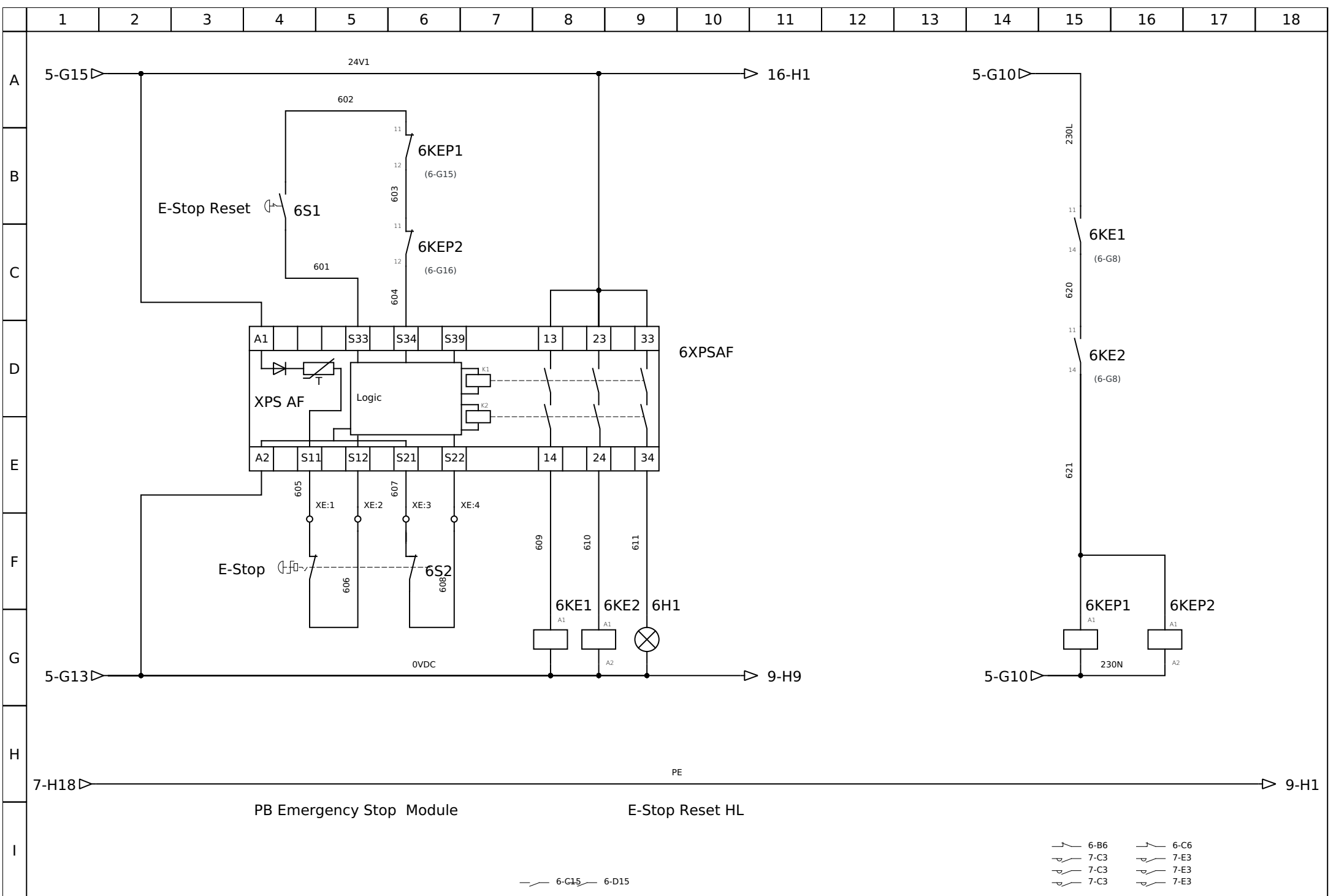
Mains Power Supply



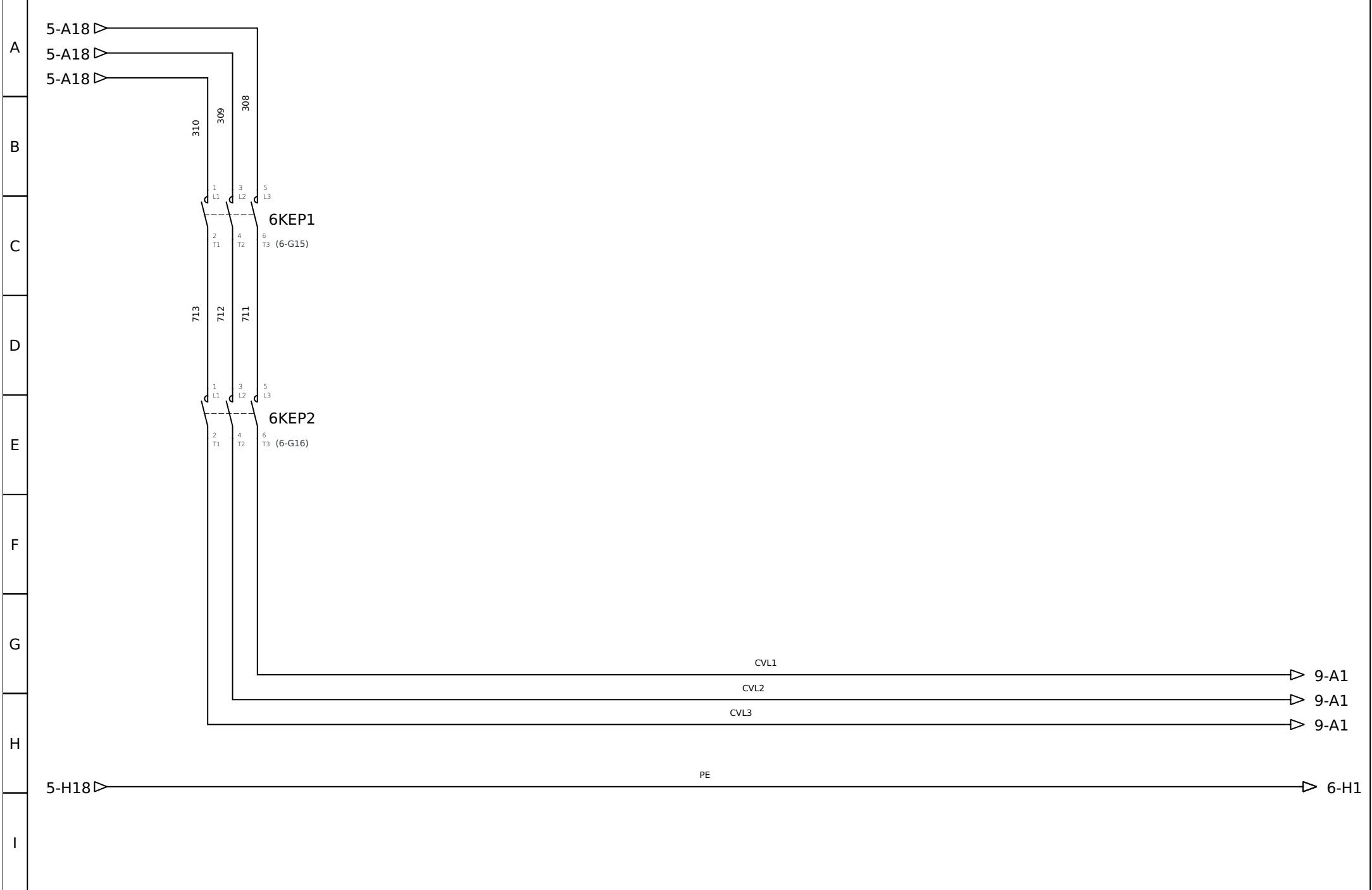
Auxiliary Transformer

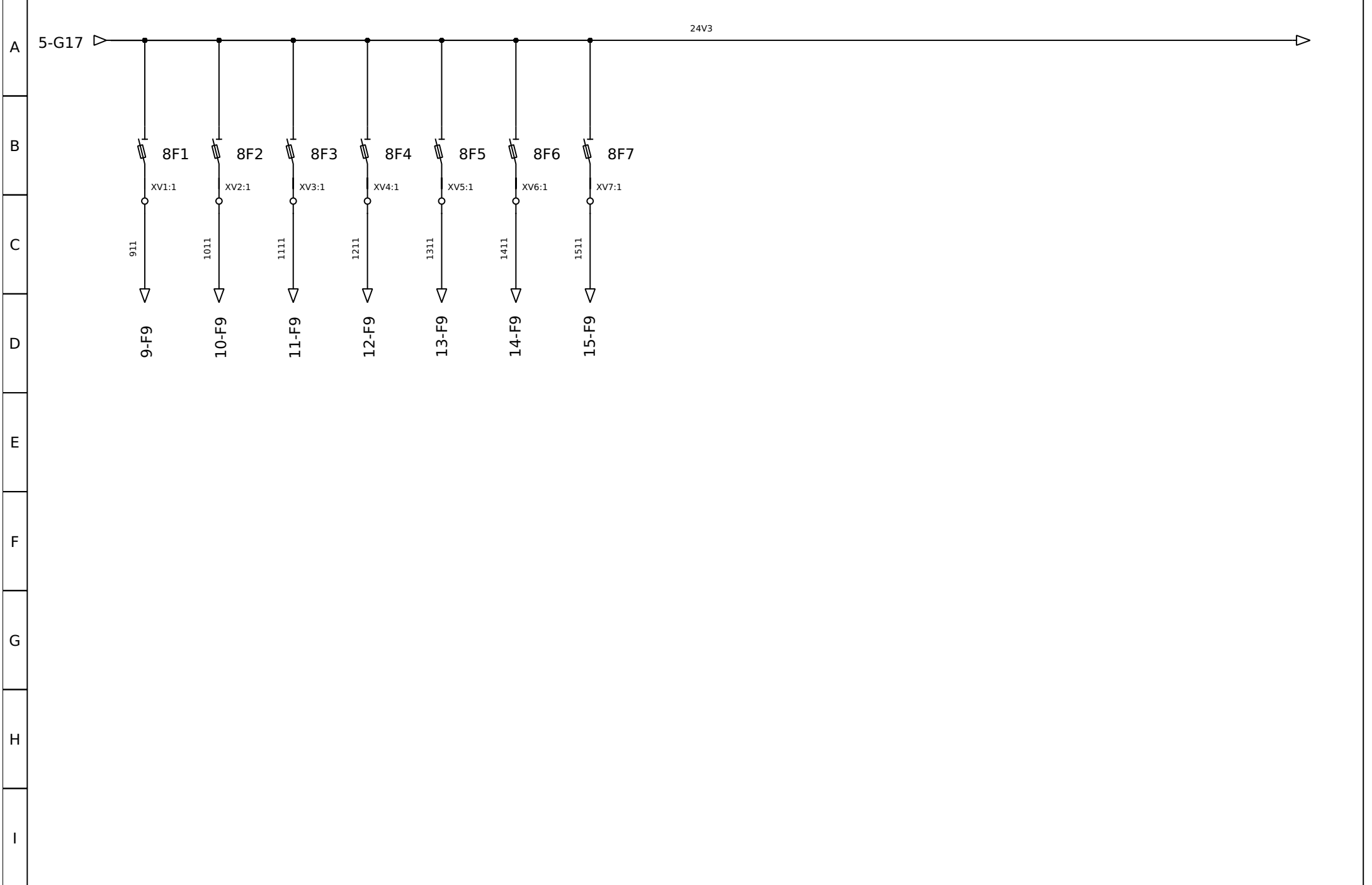
DC Power Supply

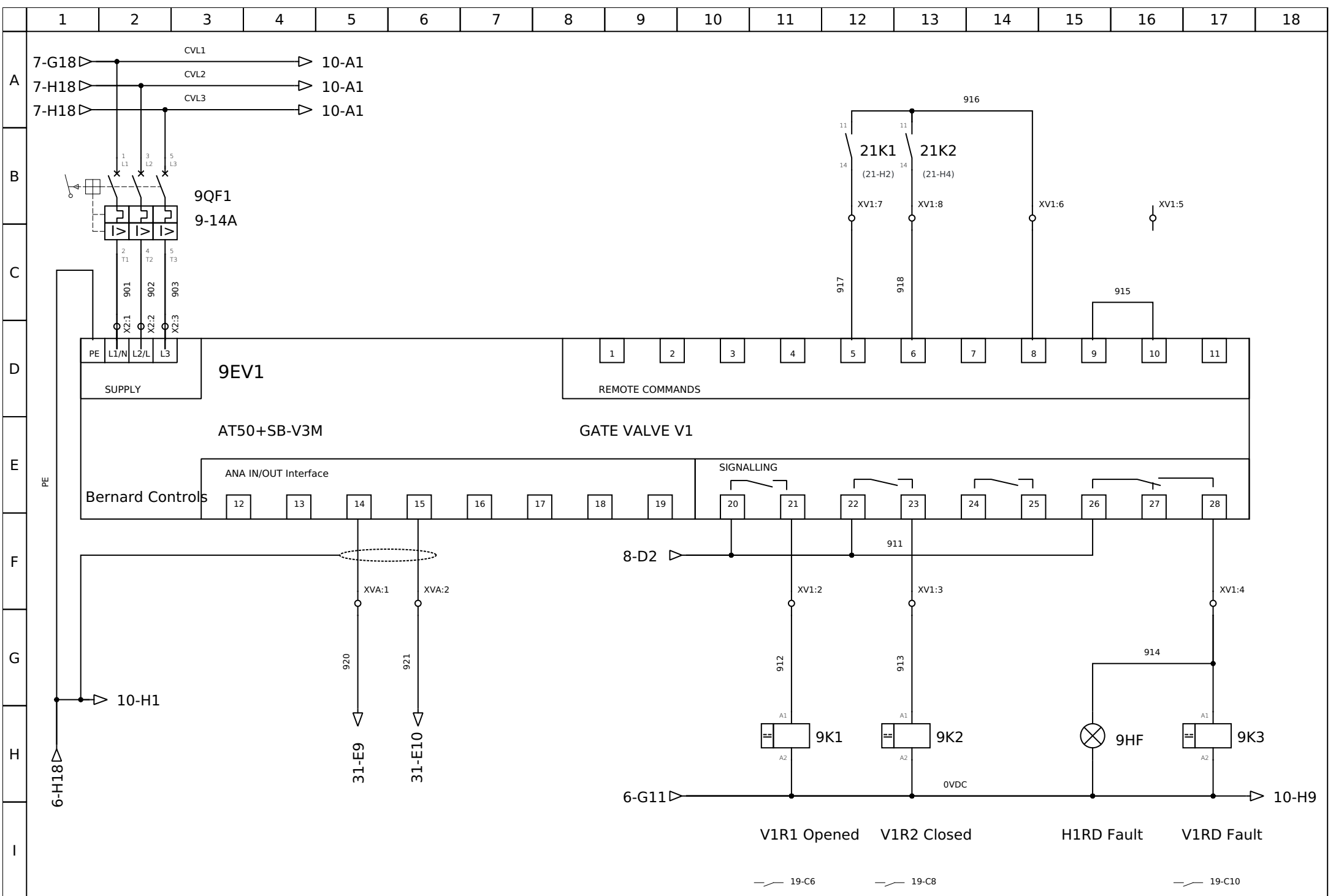
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1	1st issue		22	CHKD	AM											
REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet				=EC1	-IGC		Page: 5	
															Next: 6	



				DATE	01/09/2018				Emergency Stop Circuit		Example project		Proj N°:	
				DSGN	IM									
1	1st issue		22	CHKD	AM								Page: 6	
REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet			=EC1		-IGC	
													Next: 7	

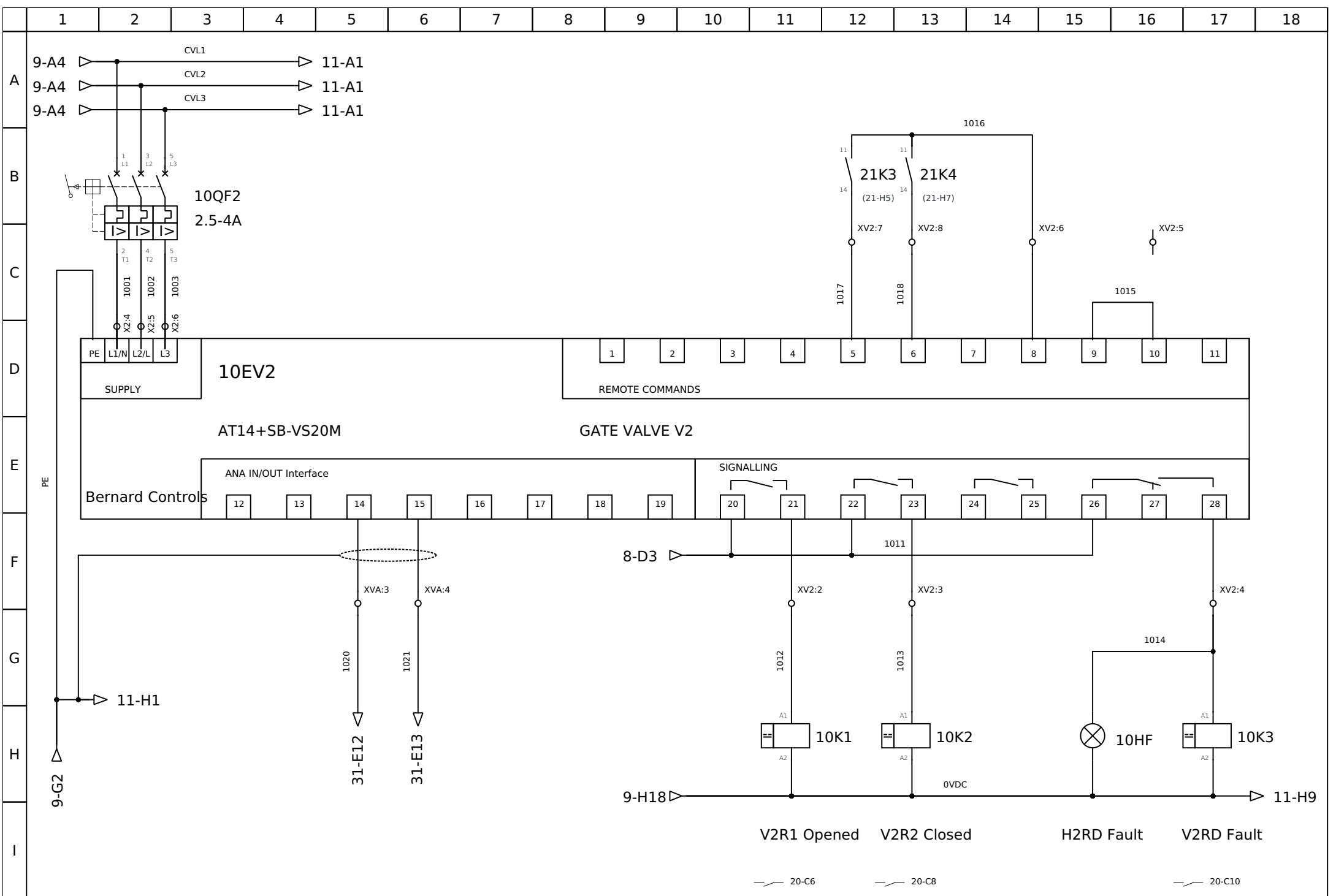






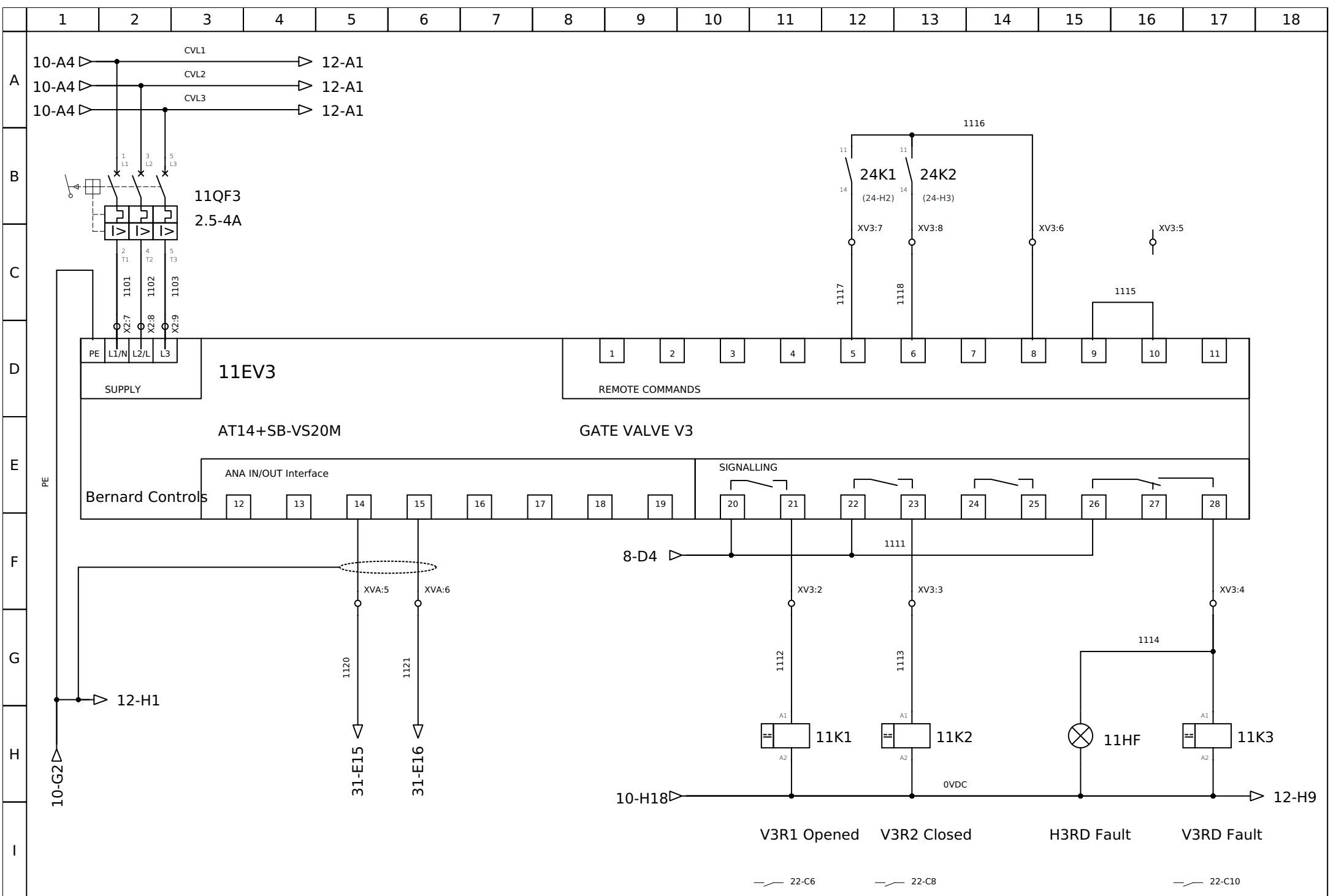
DATE	01/09/2018
DSGN	IM
CHKD	AM
APPD	AM
REV	1
MOD	1st issue
DATE	22
INIT	
APPD	AM
QET	0.7
Intake Gate Control	
Electrical Cabinet	

V1 Gate Control Circuit	Example project		Proj N°:
	=EC1	-IGC	Page: 9 Next: 10

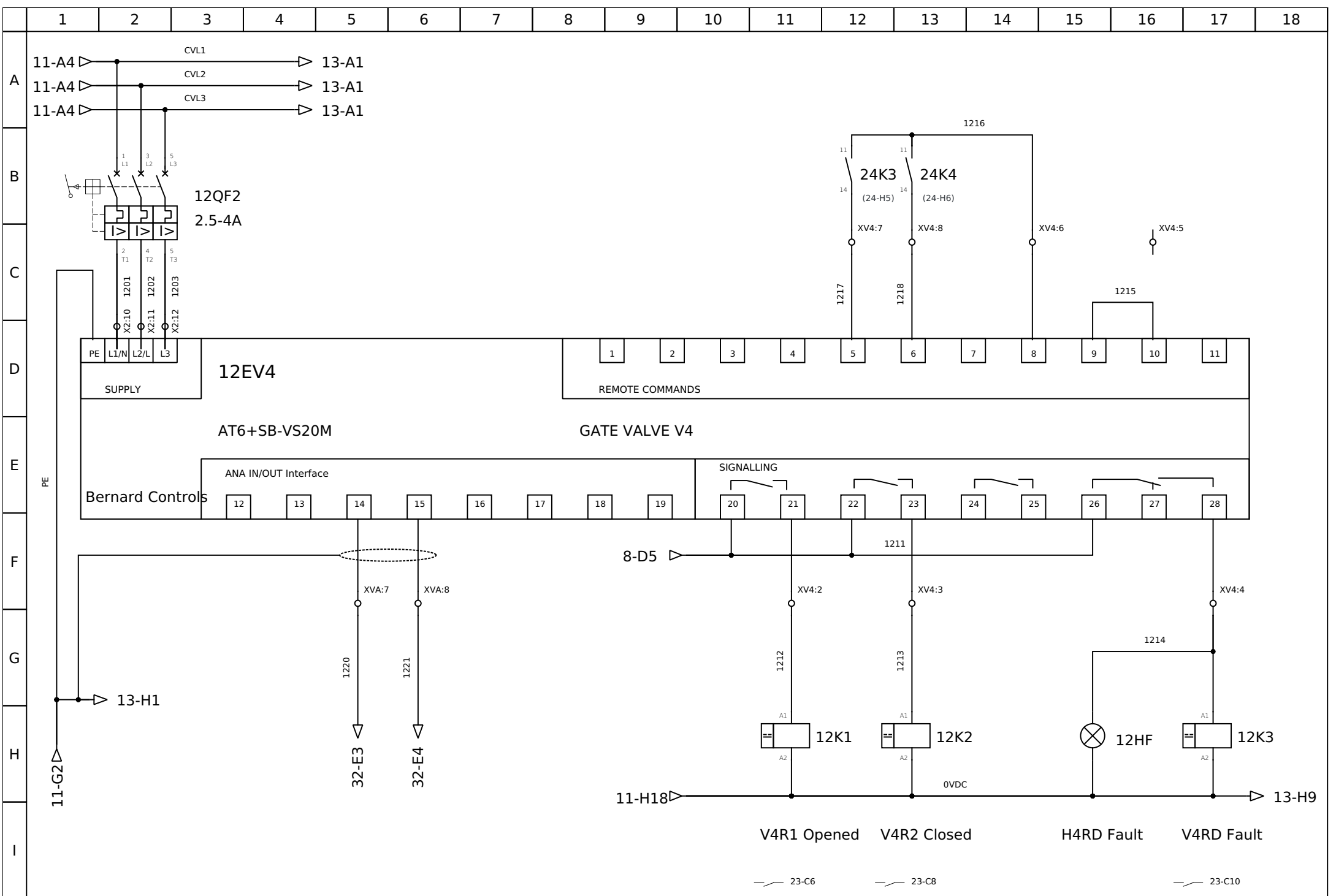


				DATE	01/09/2018							Example project		Proj N°:	
				DSGN	IM							=EC1		-IGC	
1	1st issue		22	CHKD	AM									Page: 10	
REV	MOD	DATE	INIT	APPD	AM			QET 0.7	Intake Gate Control		Electrical Cabinet		Next: 11		

V2 Gate Control Circuit

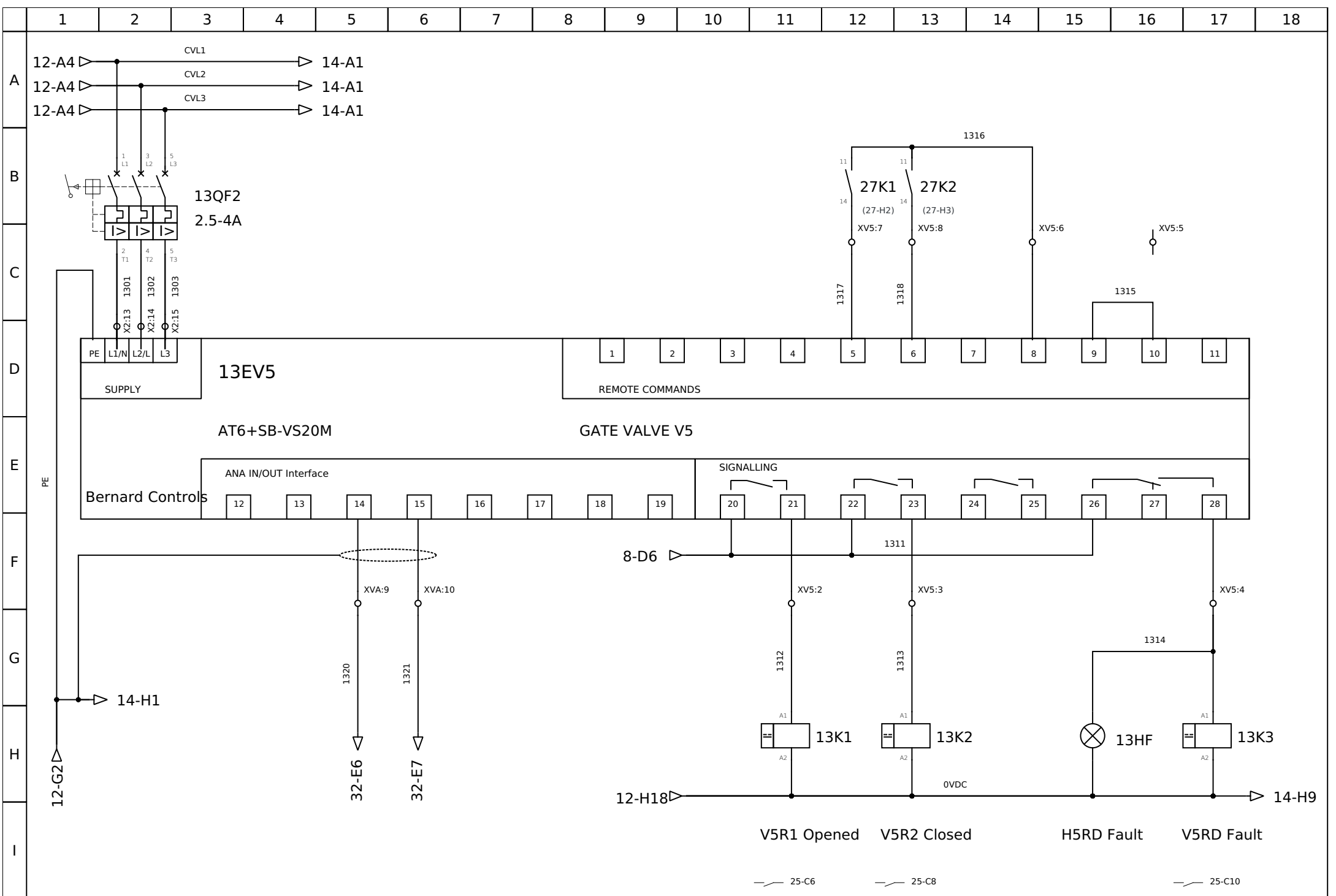


				DATE	01/09/2018				Example project				Proj N°:
				DSGN	IM				=EC1				-IGC
1	1st issue	22	CHKD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet	V3 Gate Control Circuit				Page: 11	
REV	MOD	DATE	INIT	APPD	AM							Next: 12	



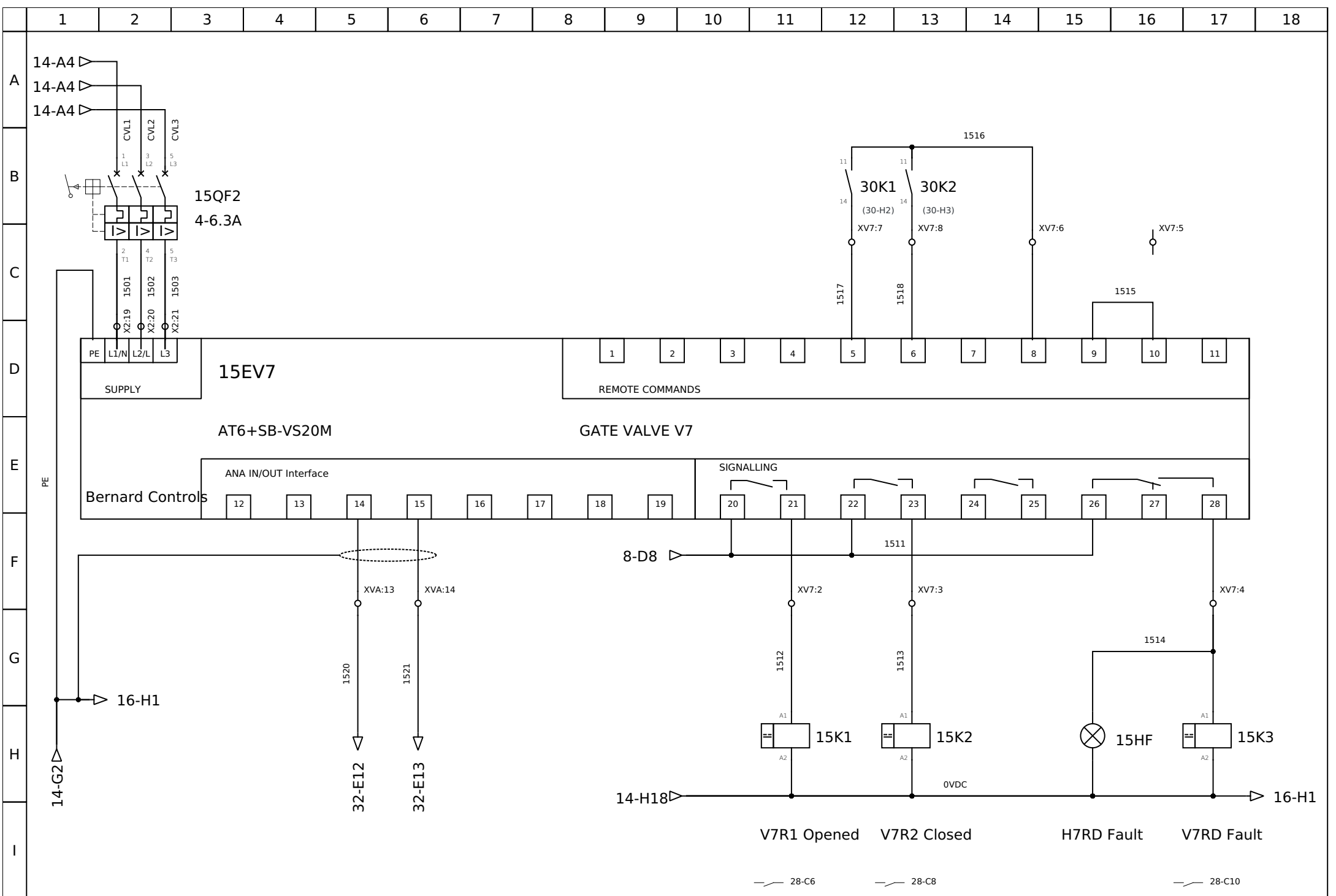
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DSGN	IM					
CHKD	AM	22				
APPD	AM					
REV	1st issue	MOD	DATE	INIT	APPD	AM
					QET 0.7	
					Intake Gate Control	Electrical Cabinet

V4 Gate Control Circuit		Example project		Proj N°:
		=EC1	-IGC	
				Page: 12
				Next: 13



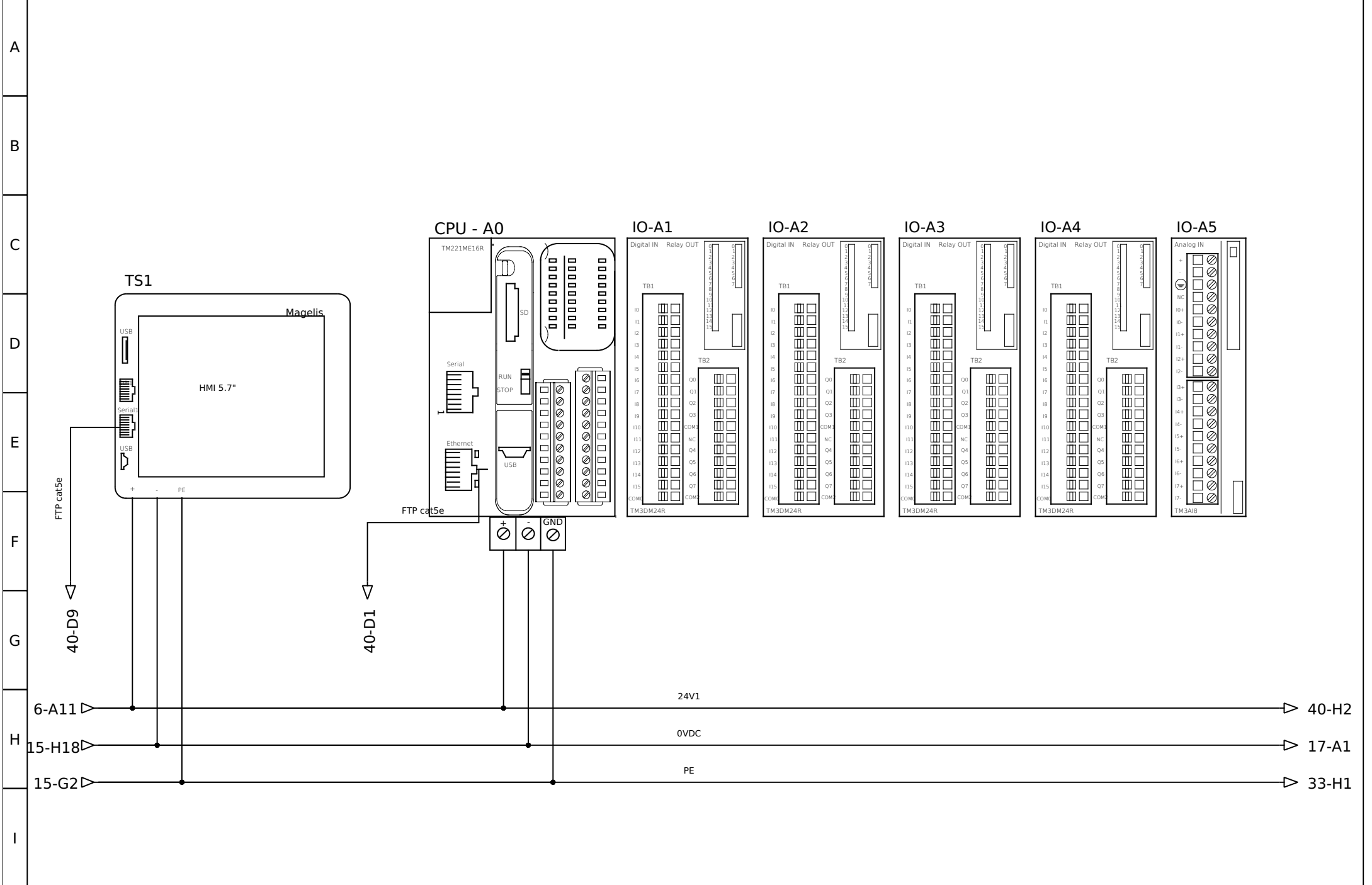
DATE	01/09/2018
DSGN	IM
CHKD	AM
APPD	AM
REV	1
MOD	1st issue
DATE	22
INIT	
APPD	AM
QET	0.7
Intake Gate Control	
Electrical Cabinet	

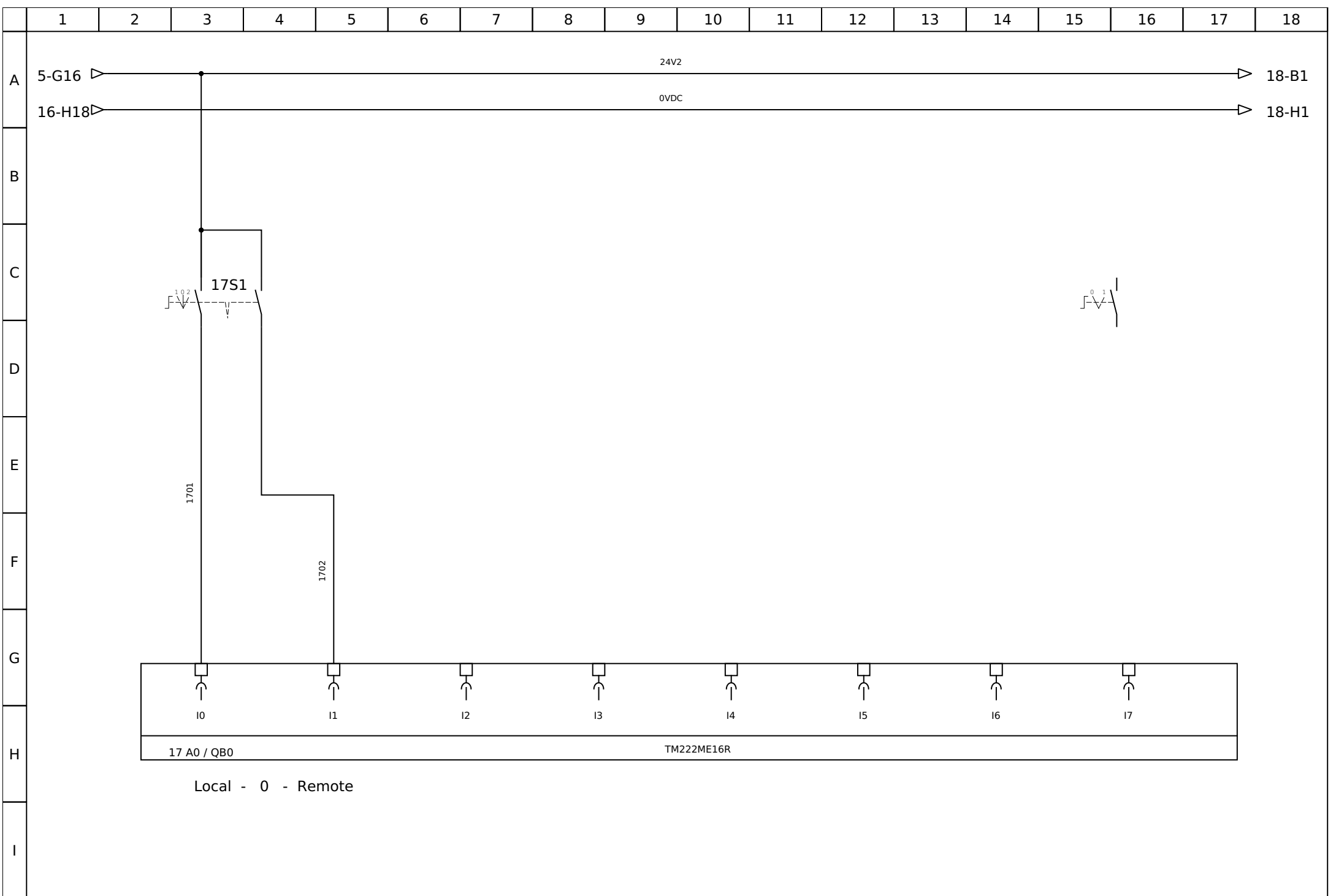
V5 Gate Control Circuit	Example project		Proj N°:
	=EC1	-IGC	Page: 13 Next: 14



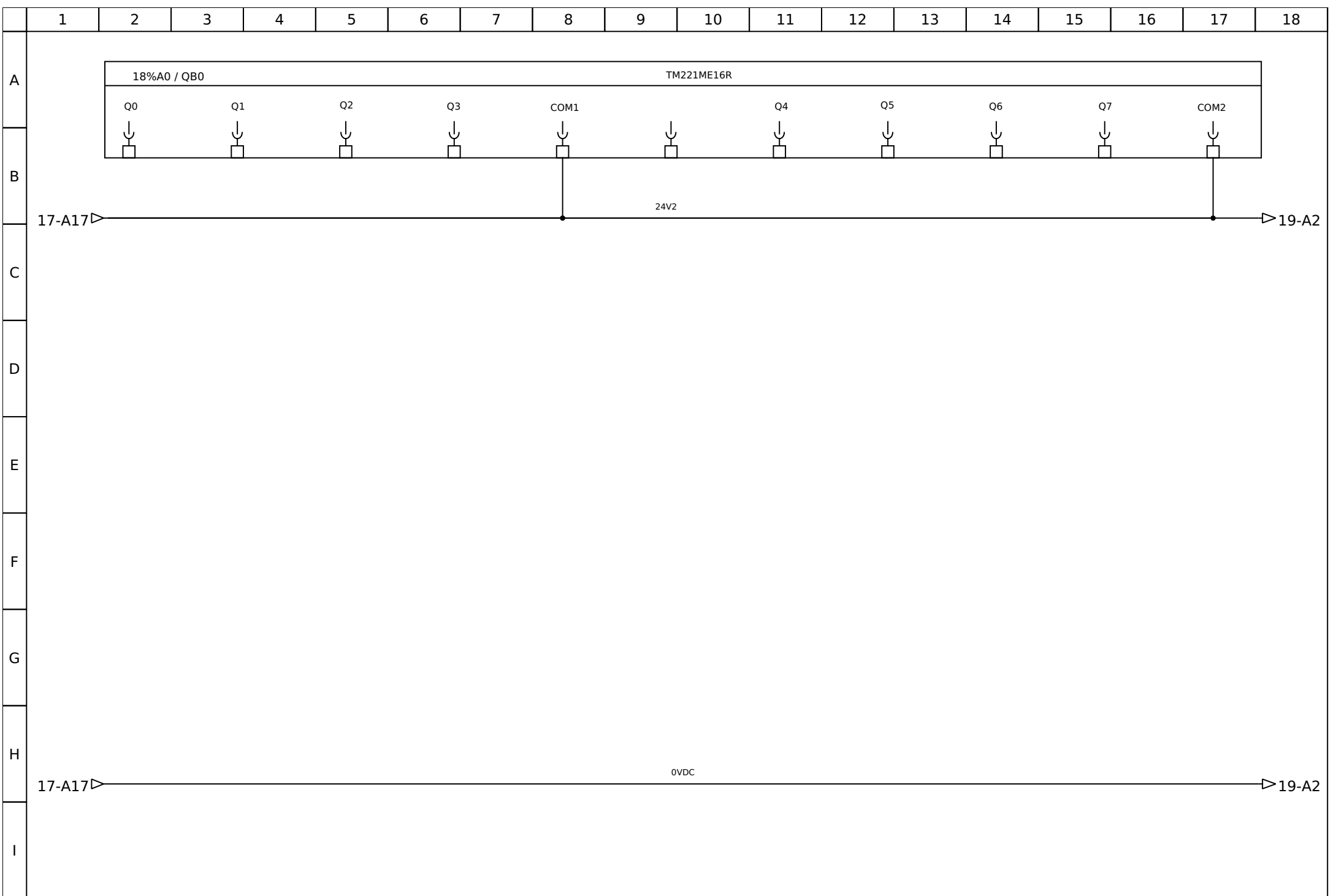
DATE	01/09/2018						
DSGN	IM						
CHKD	AM						
APPD	AM						
REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control Electrical Cabinet

V7 Gate Control Circuit		Example project		Proj N°:
		=EC1	-IGC	Page: 15 Next: 16

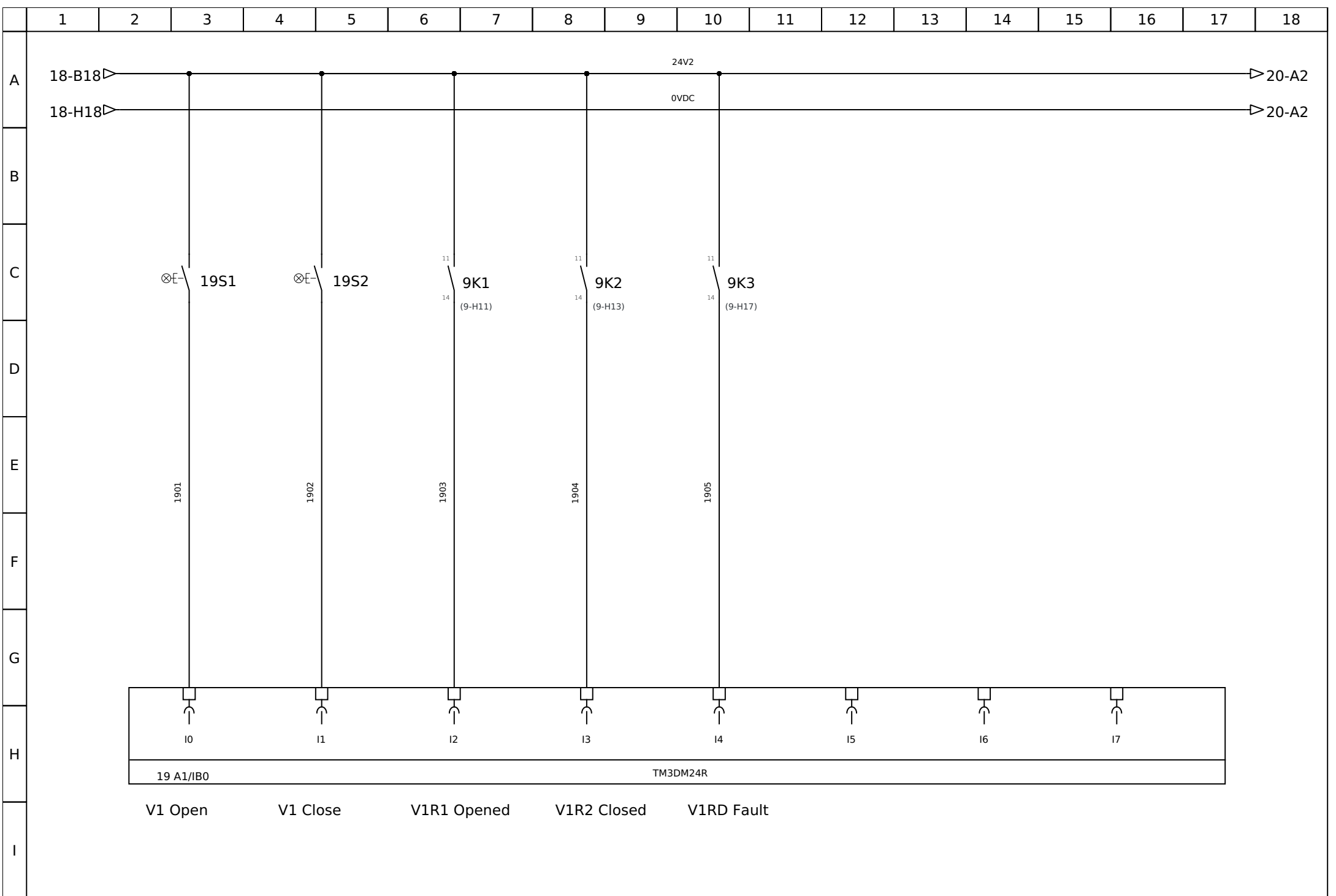




				DATE	01/09/2018				A0 Input Module	Example project		Proj N°:
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1	1st issue		22	CHKD	AM							Page: 17
REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet				Next: 18

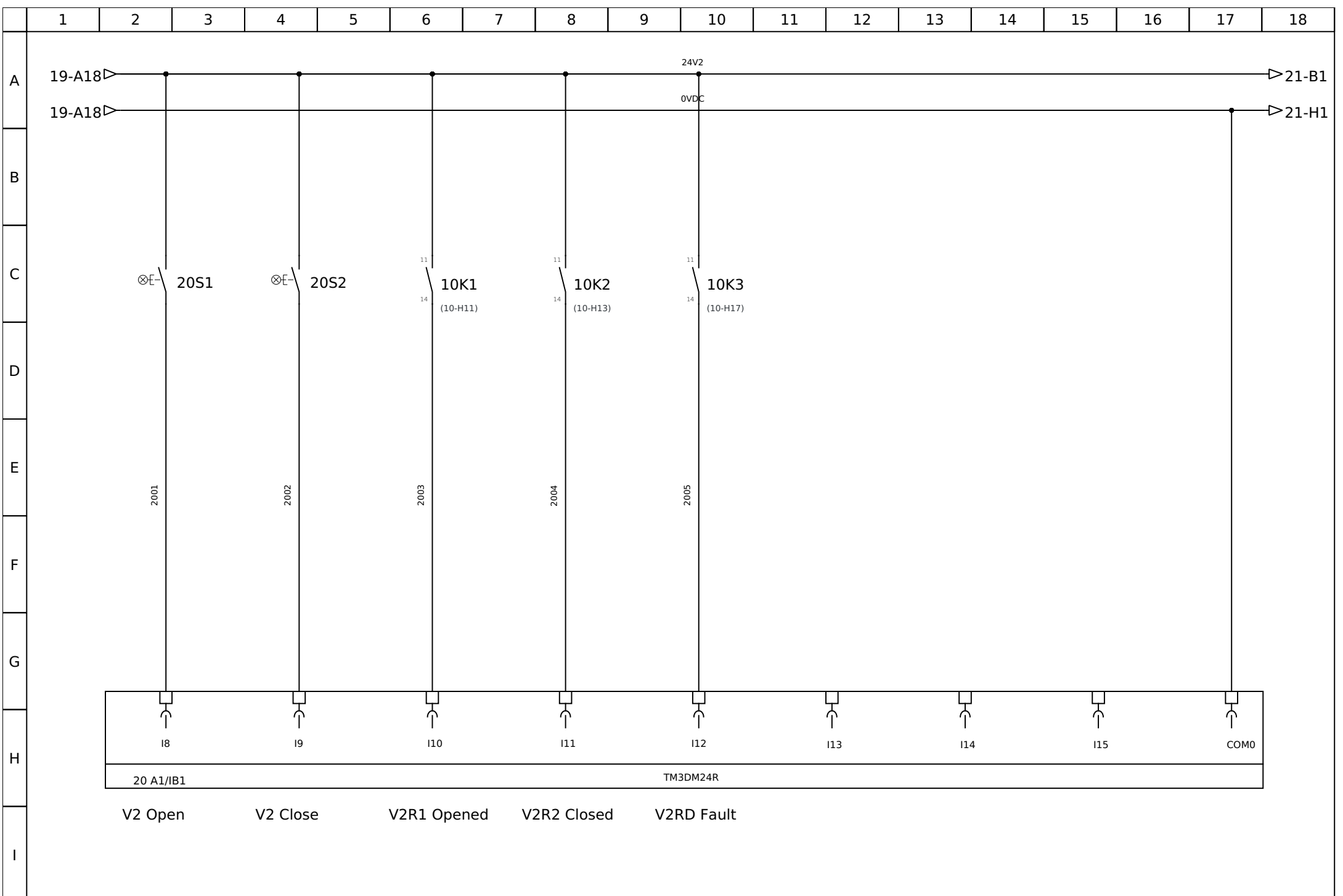


				DATE	01/09/2018			A0 Output Module			Example project		Proj N°:		
				DSGN	IM						=EC1		-IGC		Page: 18
1	1st issue		22	CHKD	AM			QET 0.7		Intake Gate Control		Electrical Cabinet		Next: 19	
REV	MOD	DATE	INIT	APPD	AM										



				DATE	01/09/2018							Example project		Proj N°:	
				DSGN	IM										
1	1st issue		22	CHKD	AM									Page: 19	
REV	MOD	DATE	INIT	APPD	AM			QET 0.7	Intake Gate Control	Electrical Cabinet		=EC1	-IGC		Next: 20

A1/1 Input Module

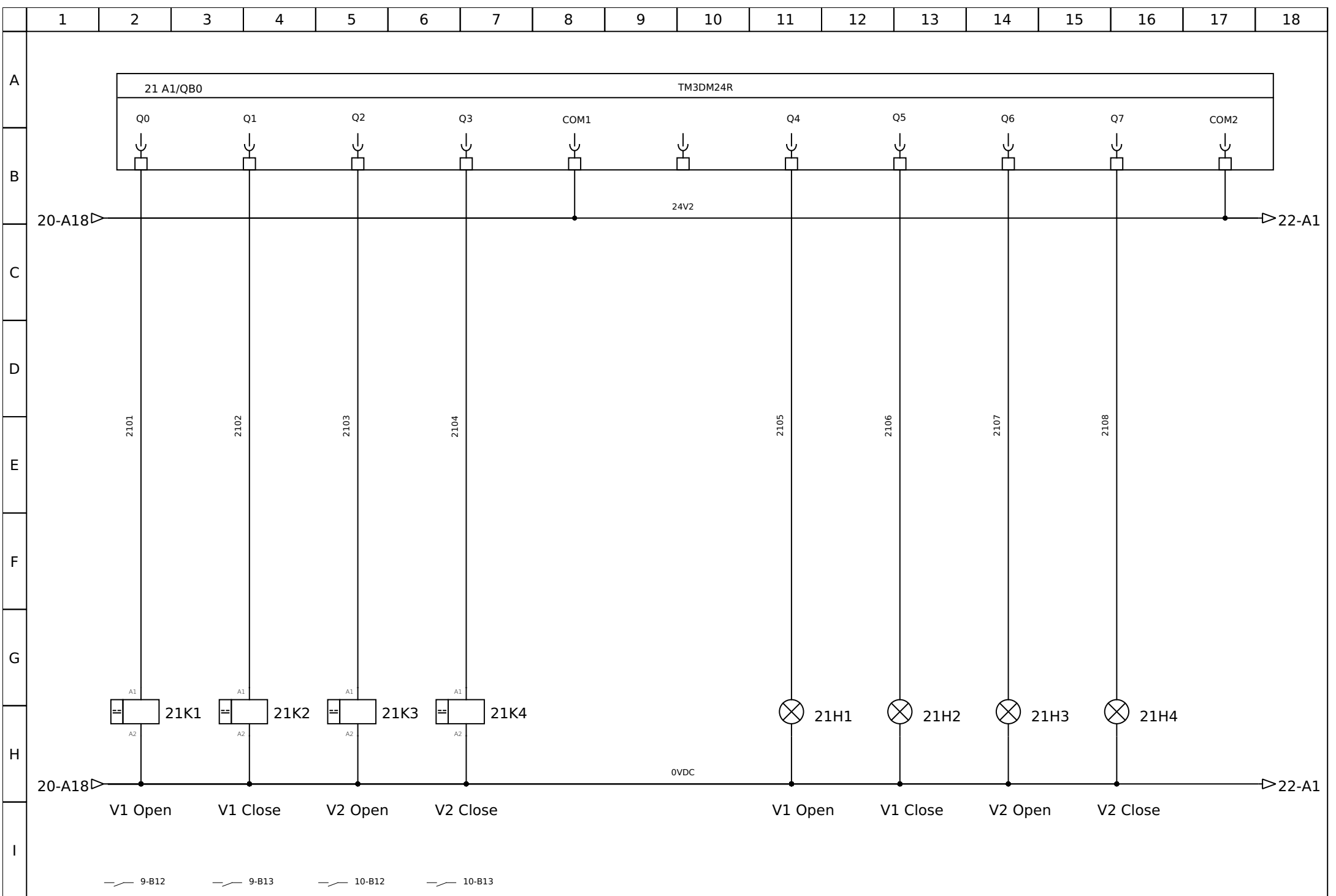


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				DSGN	IM											
1	1st issue		22	CHKD	AM									Page: 20		
REV	MOD	DATE	INIT	APPD	AM			QET 0.7	Intake Gate Control	Electrical Cabinet			=EC1	-IGC	Next: 21	

A1/2 Input Module

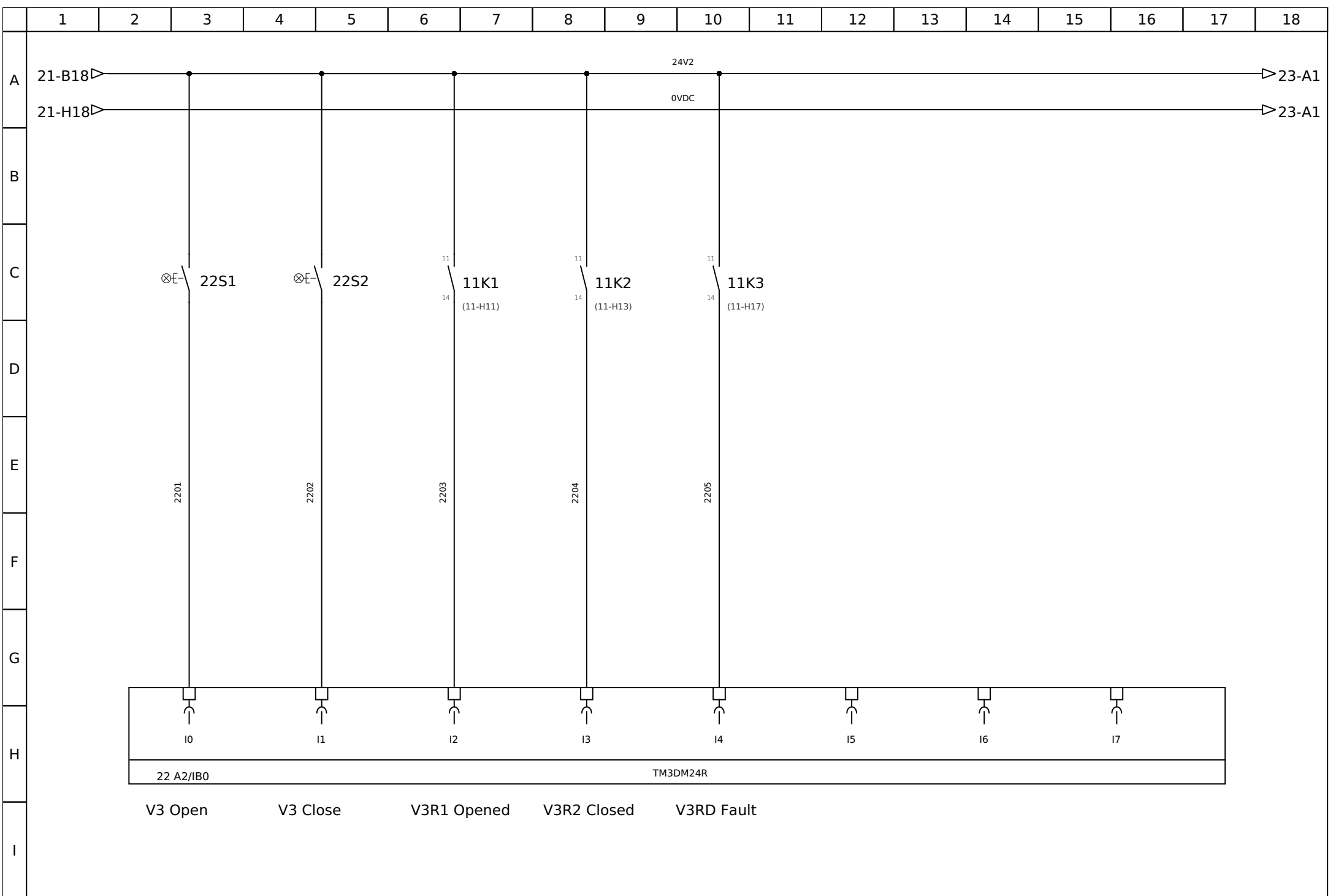
=EC1

-IGC

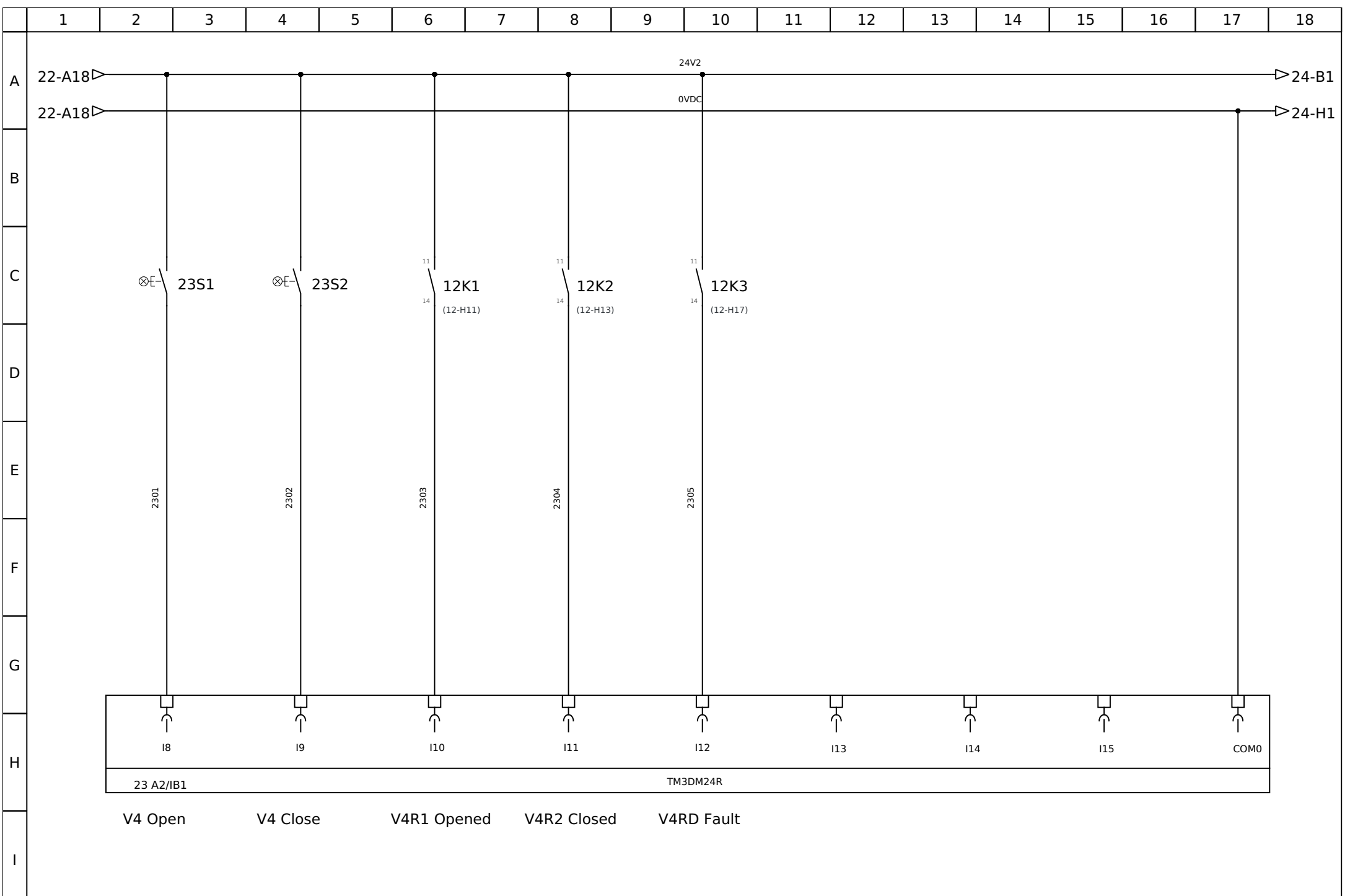


				DATE	01/09/2018				Example project		Proj N°:	
				DSGN	IM				=EC1		-IGC	
1	1st issue	22	CHKD	AM							Page: 21	
REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet			Next: 22	

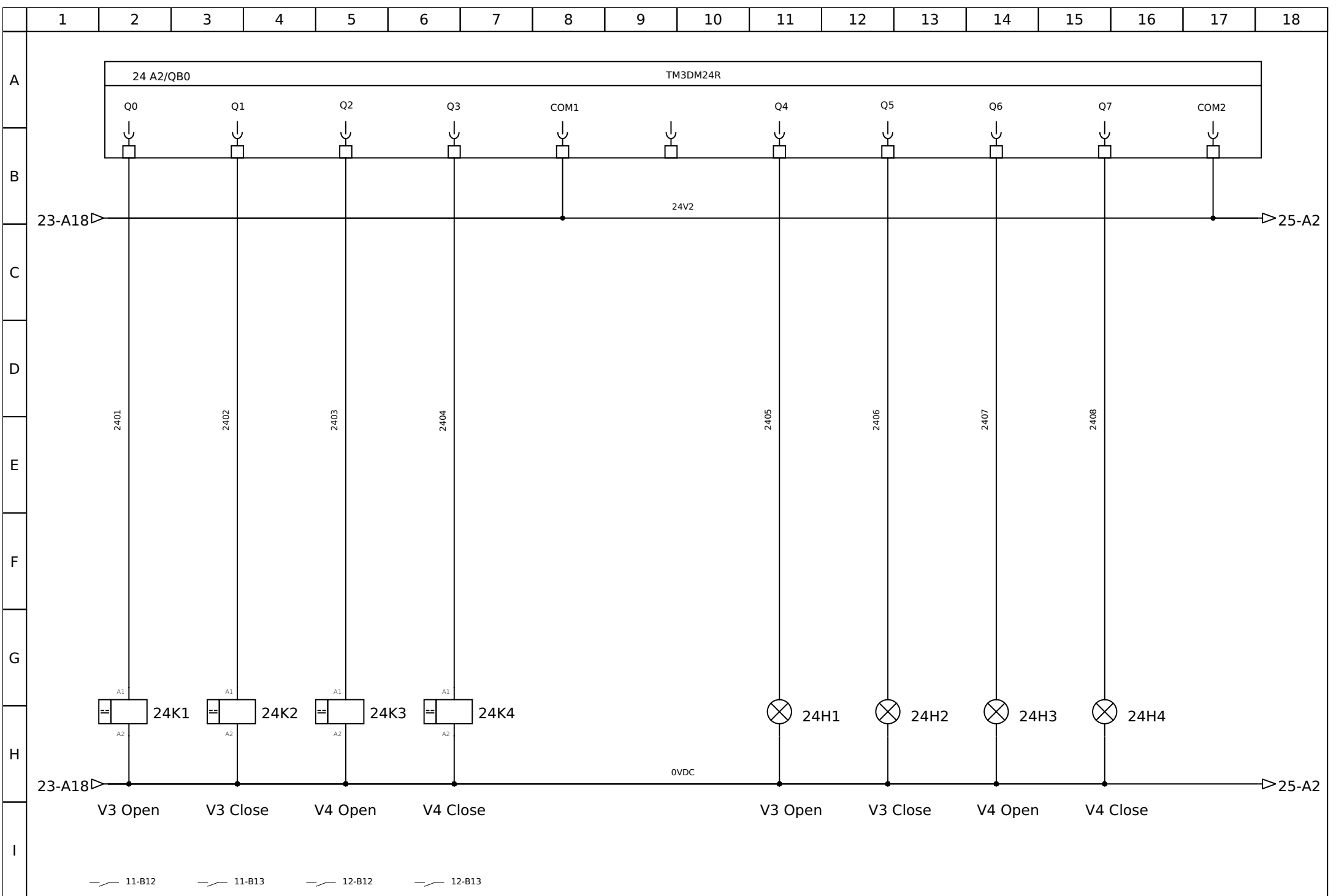
A1 Output Module



				DATE	01/09/2018				A2/1 Input Module		Example project		Proj N°:	
				DSGN	IM									
1	1st issue		22	CHKD	AM								Page: 22	
REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet			=EC1	-IGC		Next: 23



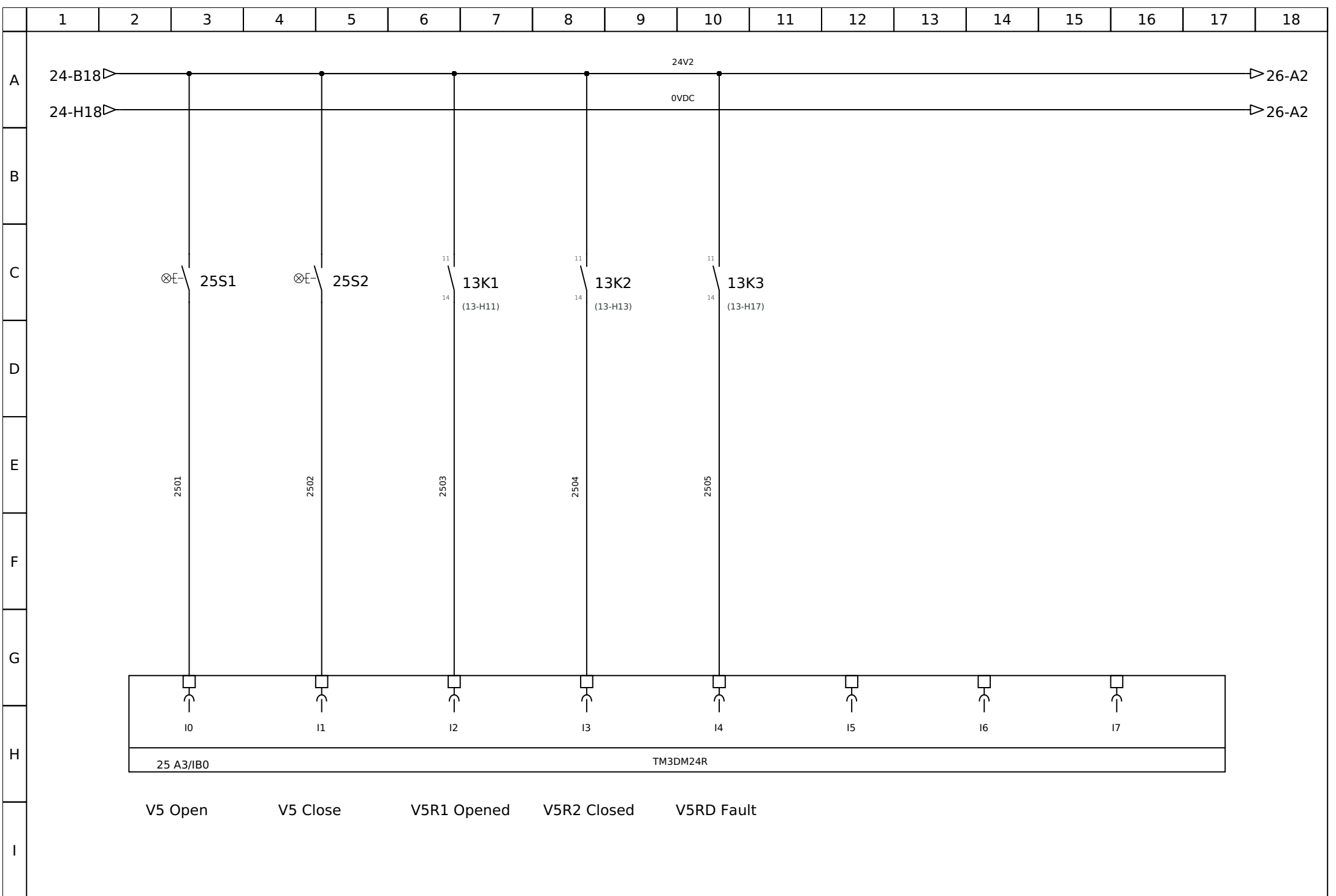
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1	1st issue		22	CHKD	AM								Page: 23	
REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet			=EC1	-IGC		Next: 24



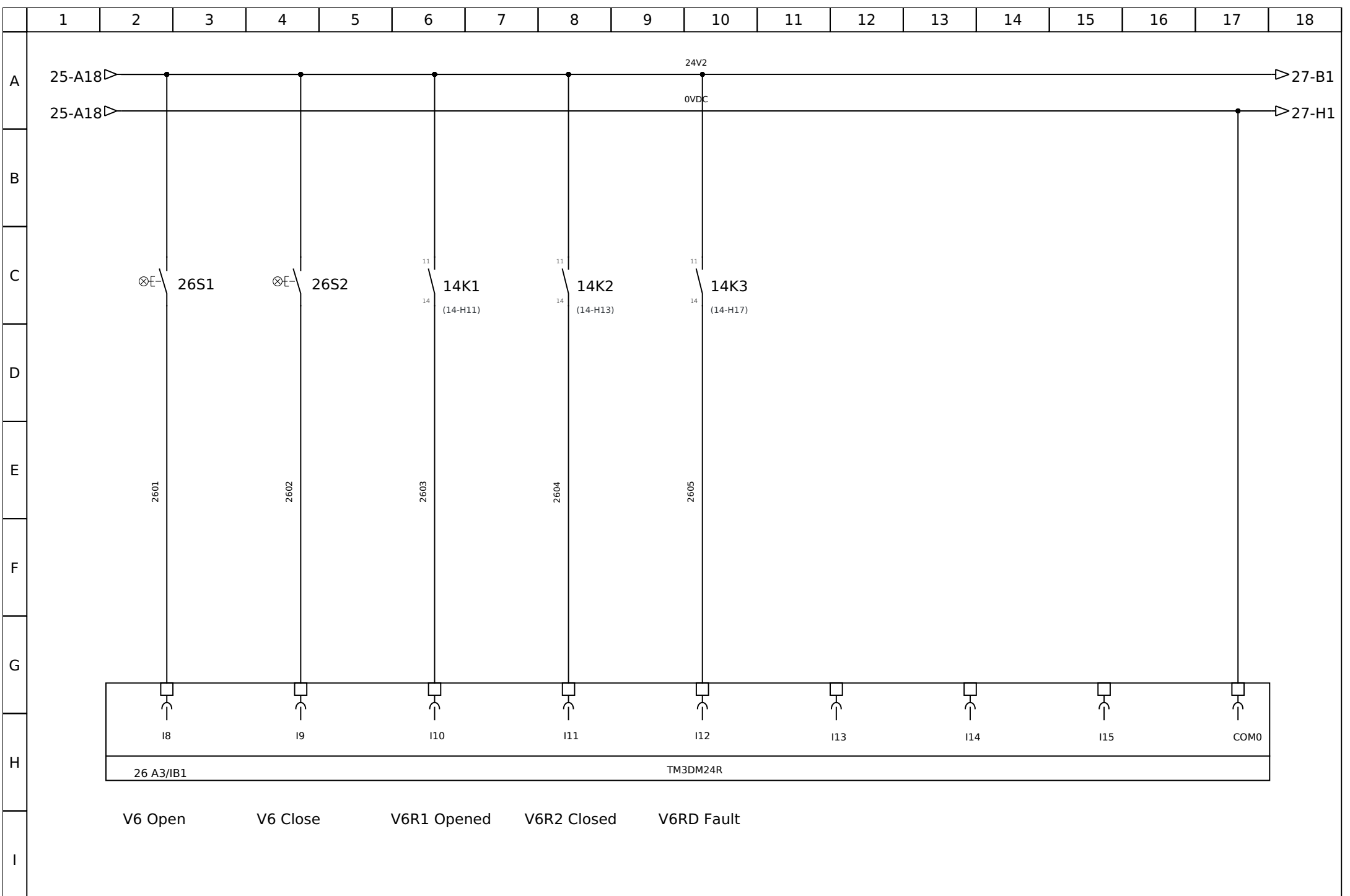
DATE	01/09/2018						
DSGN	IM						
CHKD	AM						
APPD	AM						
REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control
							Electrical Cabinet

A2 Output Module		Example project		Proj N°:
		=EC1	-IGC	Page: 24 Next: 25

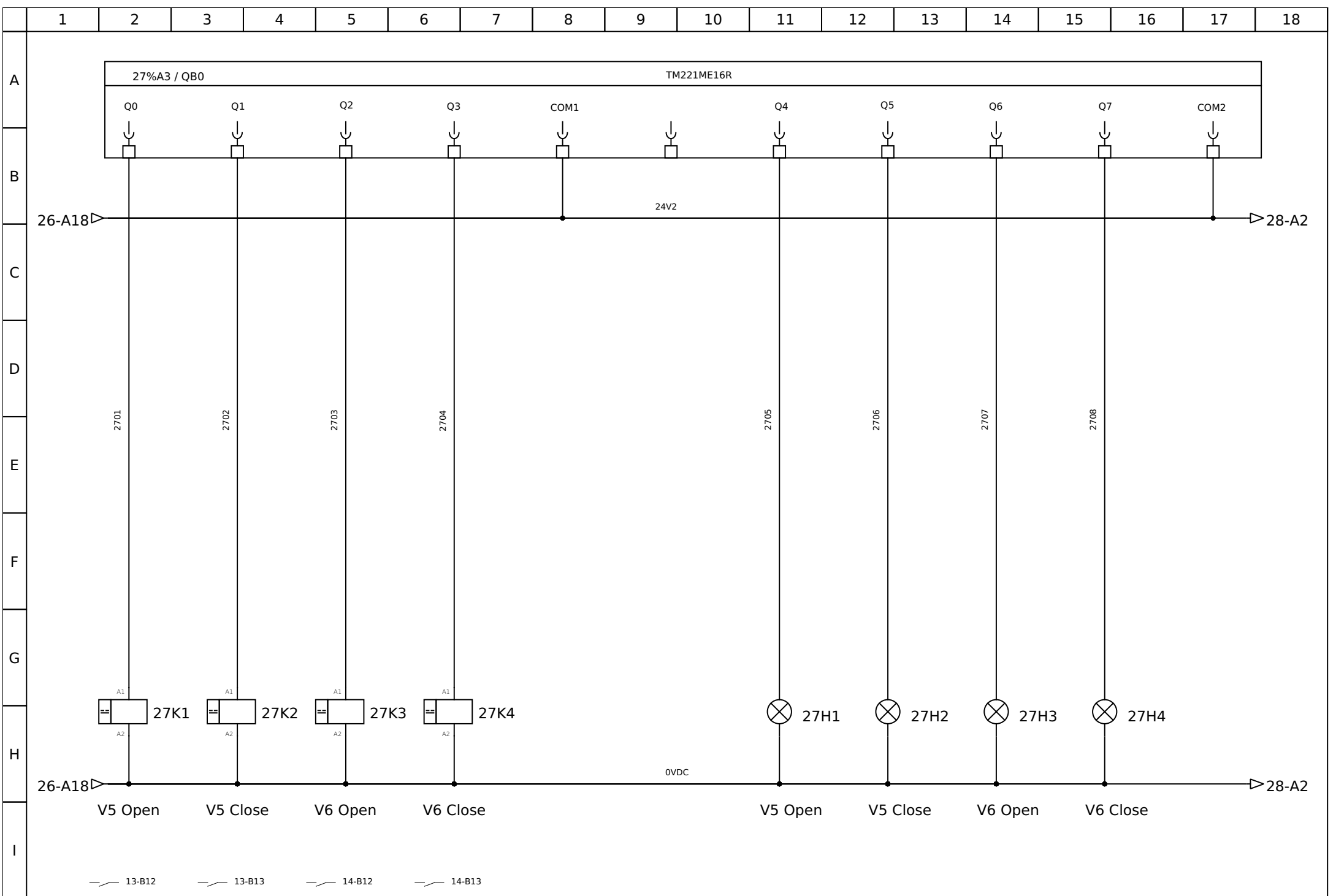
11-B12
 11-B13
 12-B12
 12-B13



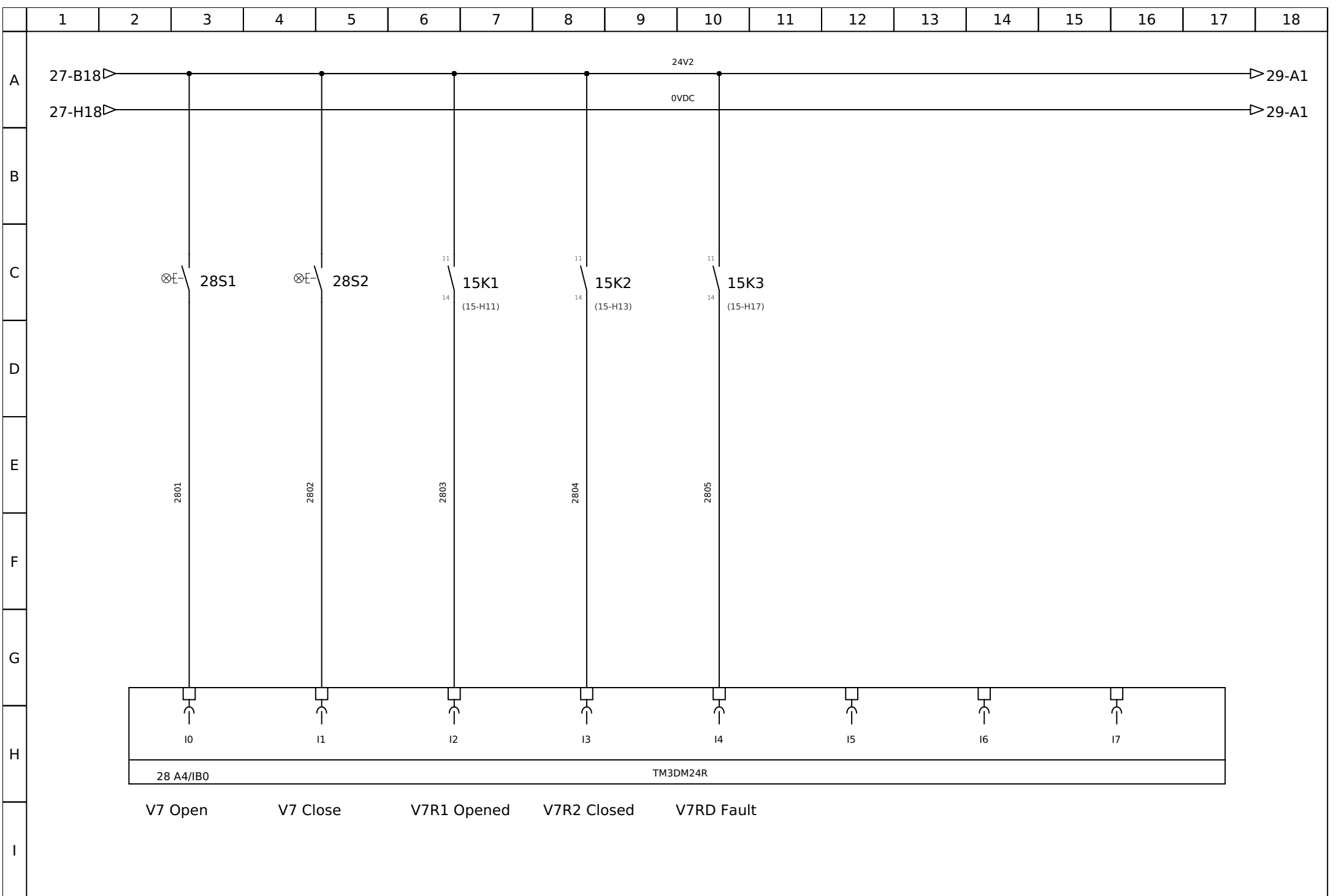
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1	1st issue		22	CHKD	AM									
REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet			=EC1	-IGC		Page: 25 Next: 26



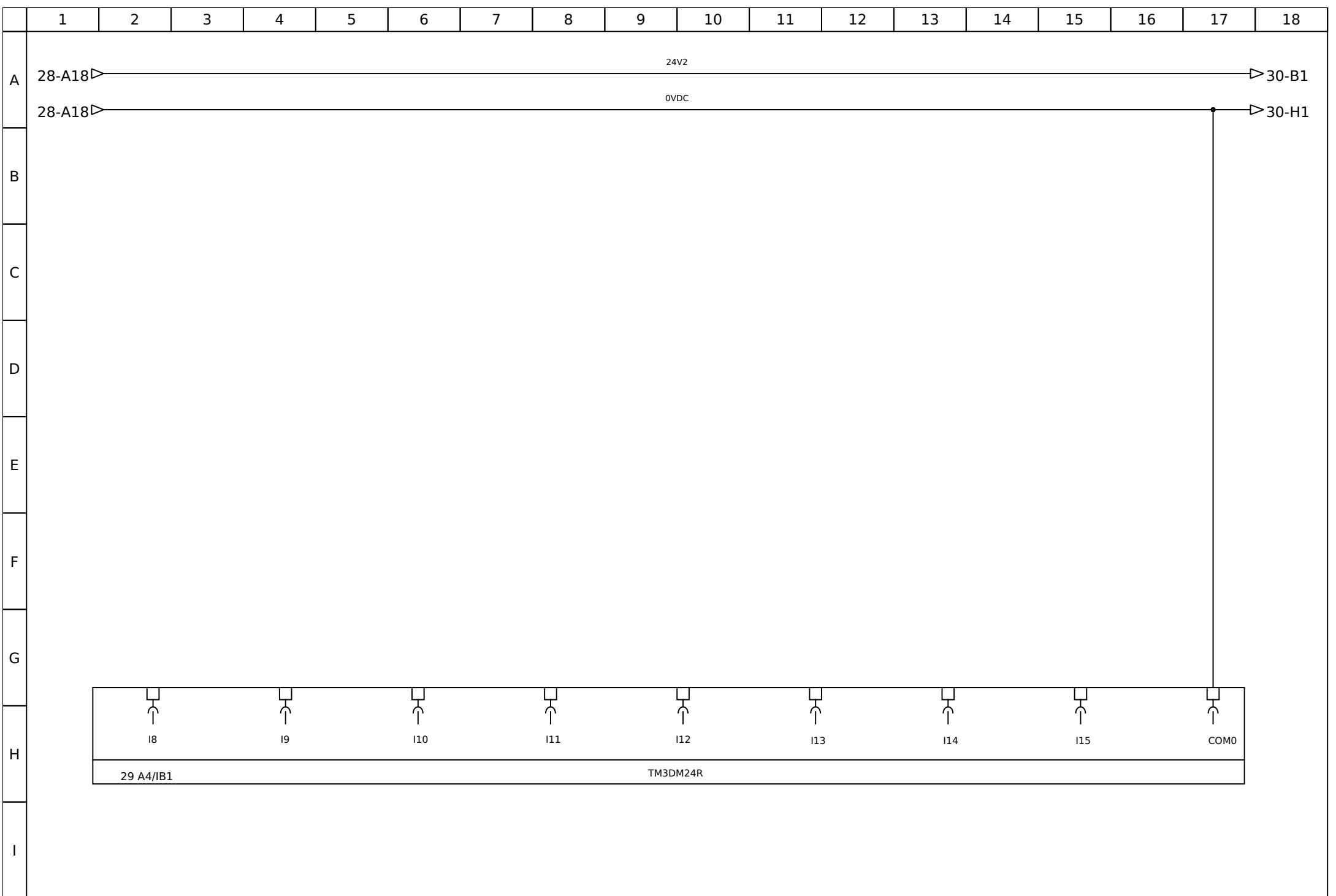
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				DSGN	IM						=EC1				
1	1st issue		22	CHKD	AM			A3/2 Input Module						Page: 26	
REV	MOD	DATE	INIT	APPD	AM			QET 0.7			Intake Gate Control			Electrical Cabinet	
											-IGC			Next: 27	



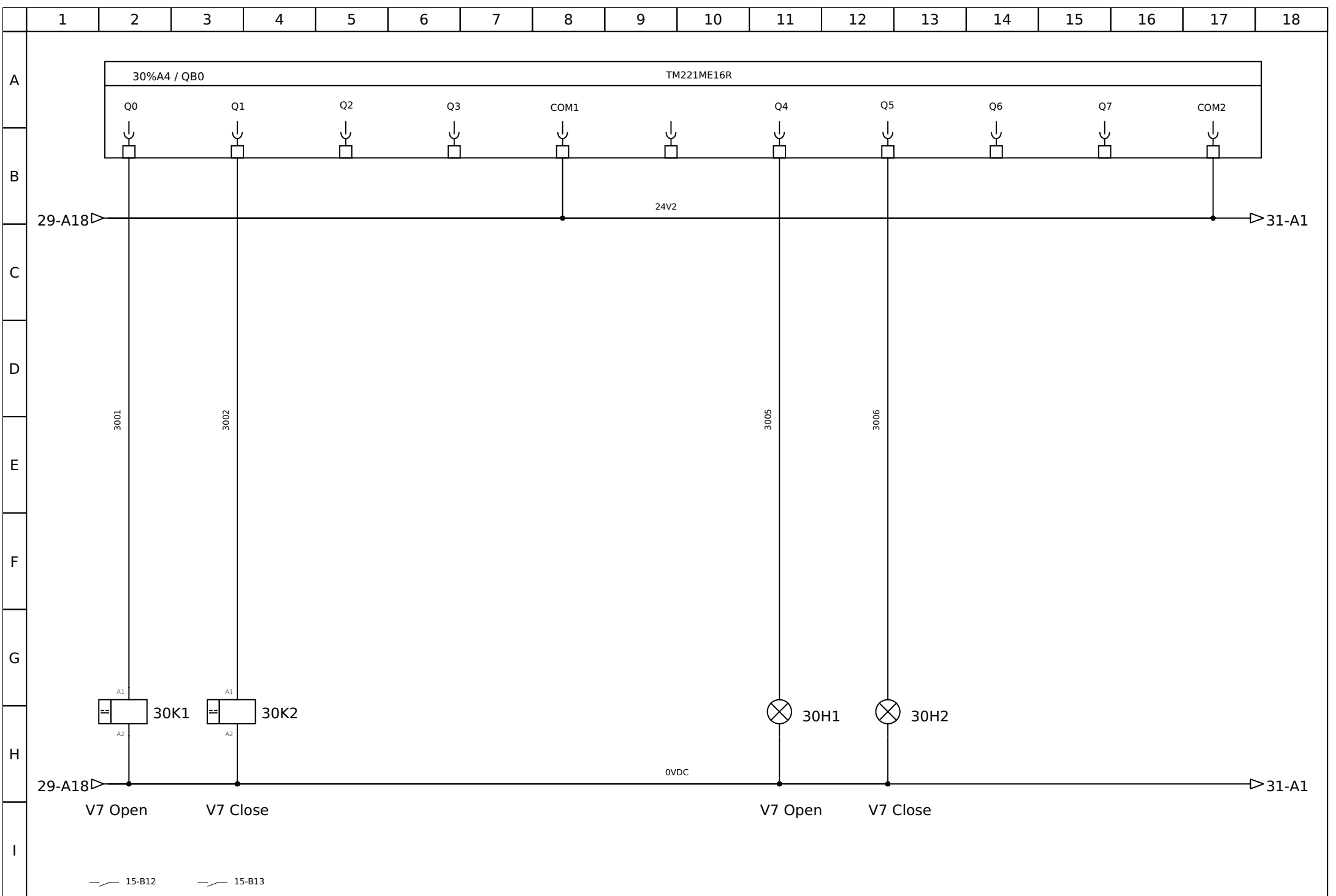
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REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet			=EC1	-IGC	Page: 27	Next: 28



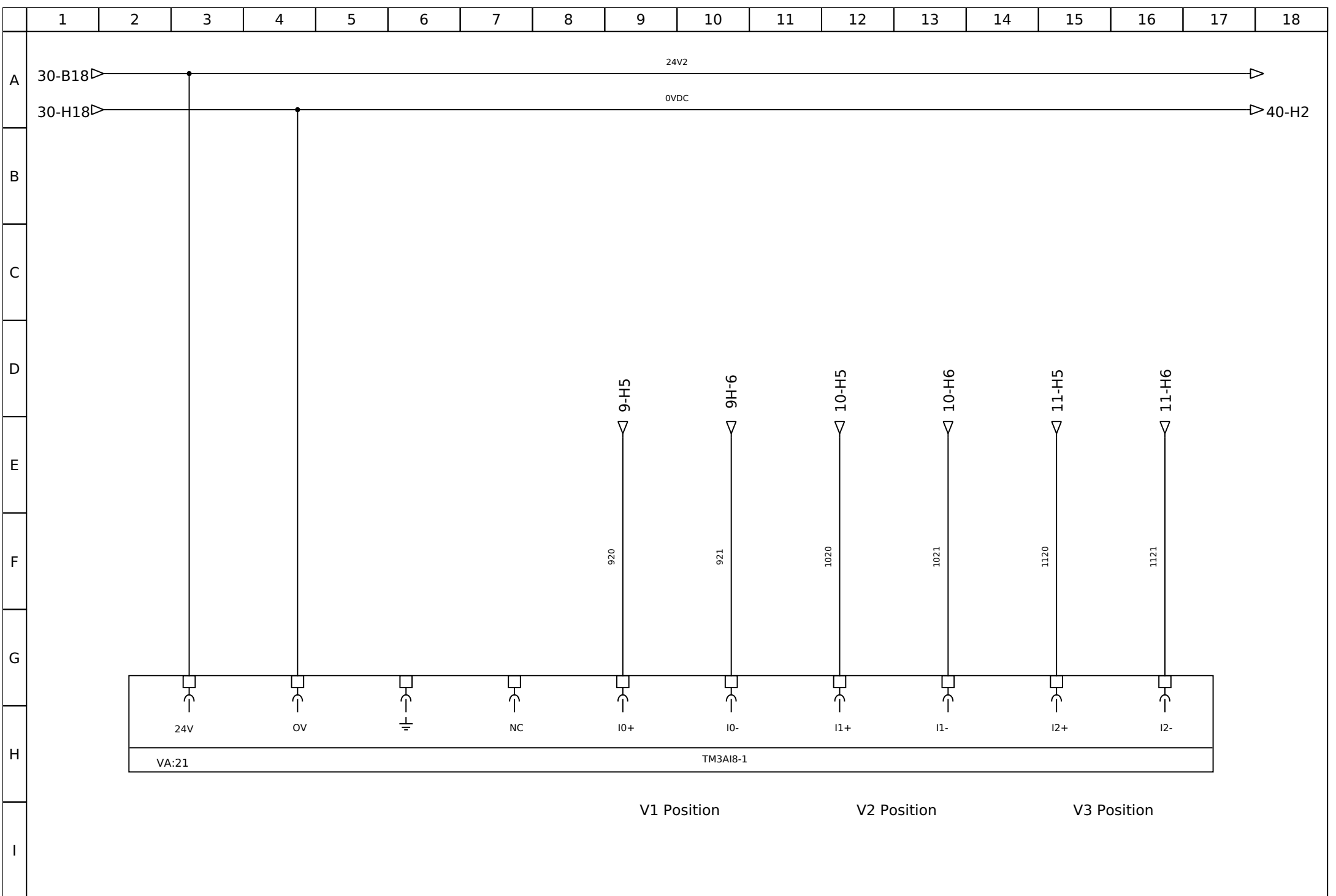
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				DSGN	IM					=EC1	-IGC	Page: 28
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REV	MOD	DATE	INIT	APPD	AM							



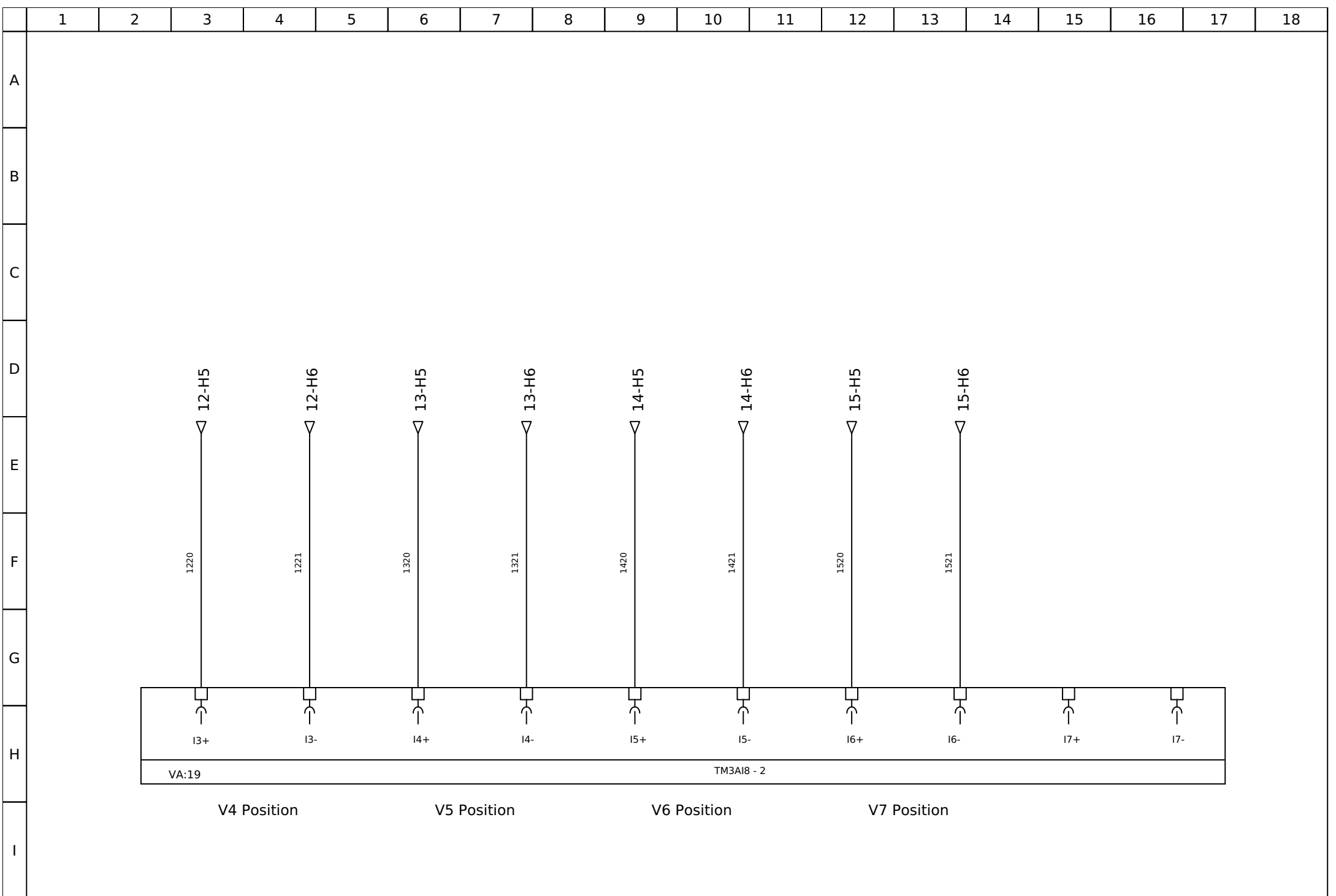
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1	1st issue		22	CHKD	AM		A4/2 Input Module						Page: 29	
REV	MOD	DATE	INIT	APPD	AM		QET 0.7	Intake Gate Control	Electrical Cabinet				Next: 30	



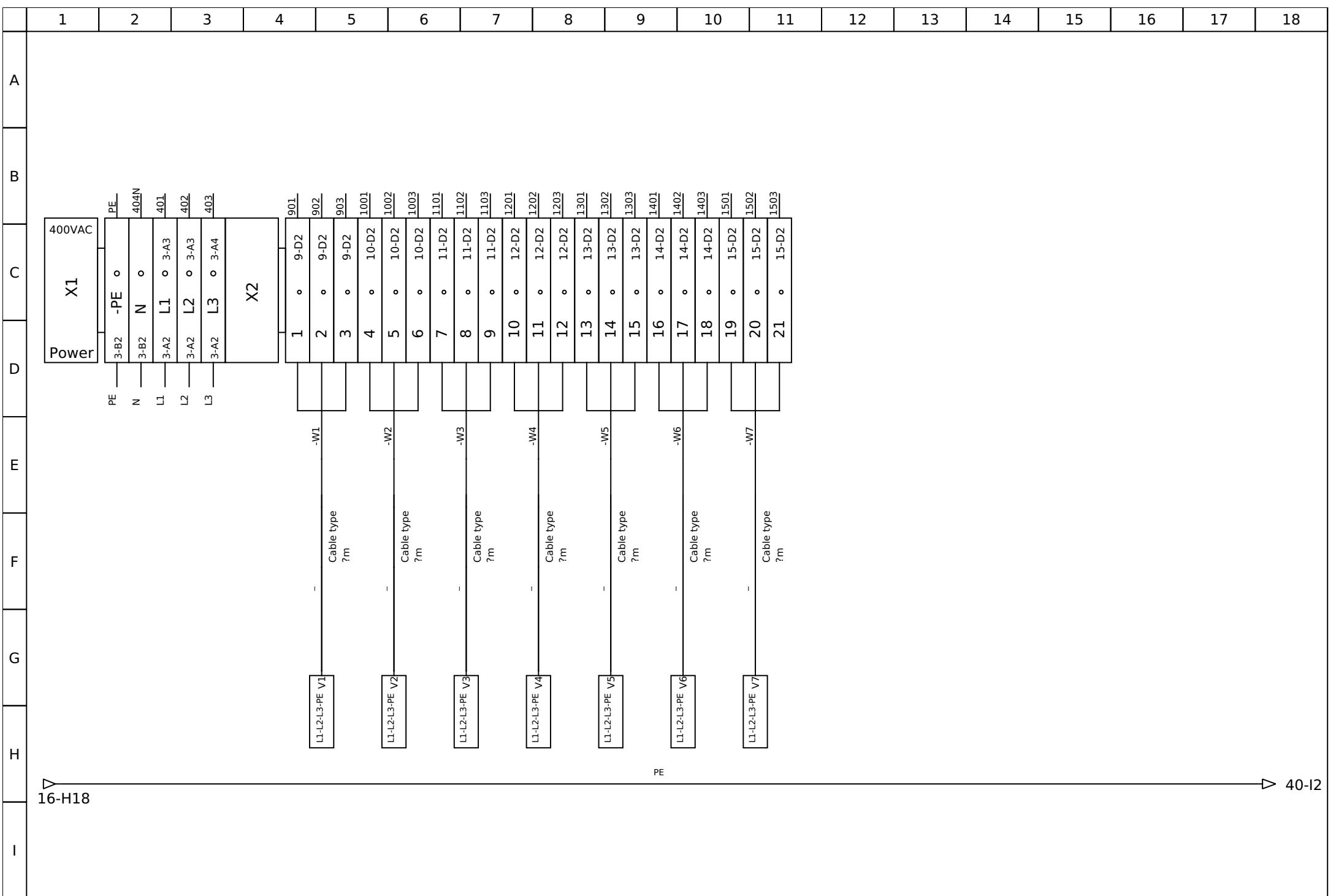
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REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet			=EC1	-IGC	Page: 30	Next: 31



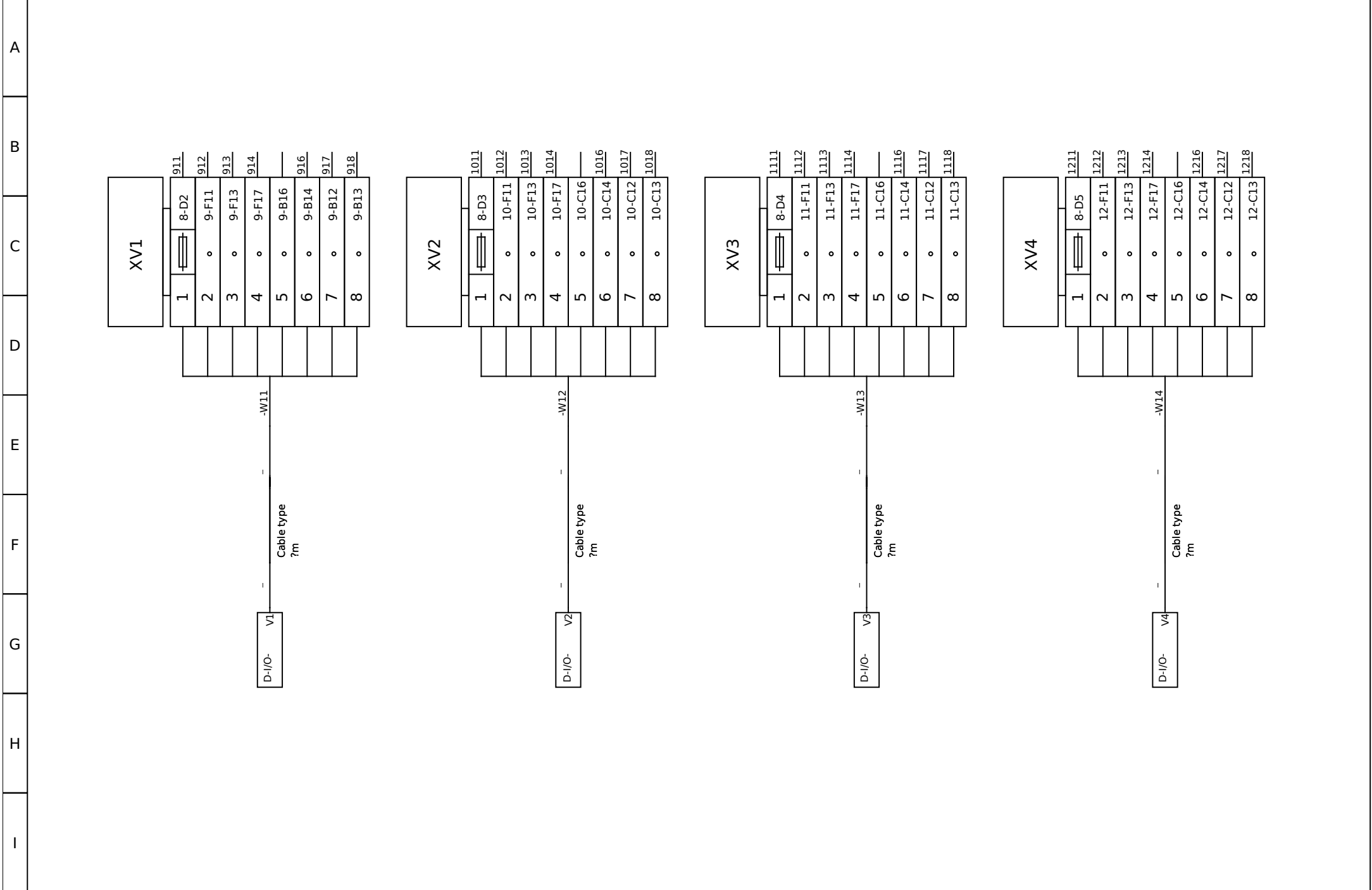
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1	1st issue		22	CHKD	AM											
REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet				=EC1	-IGC		Page: 31 Next: 32	

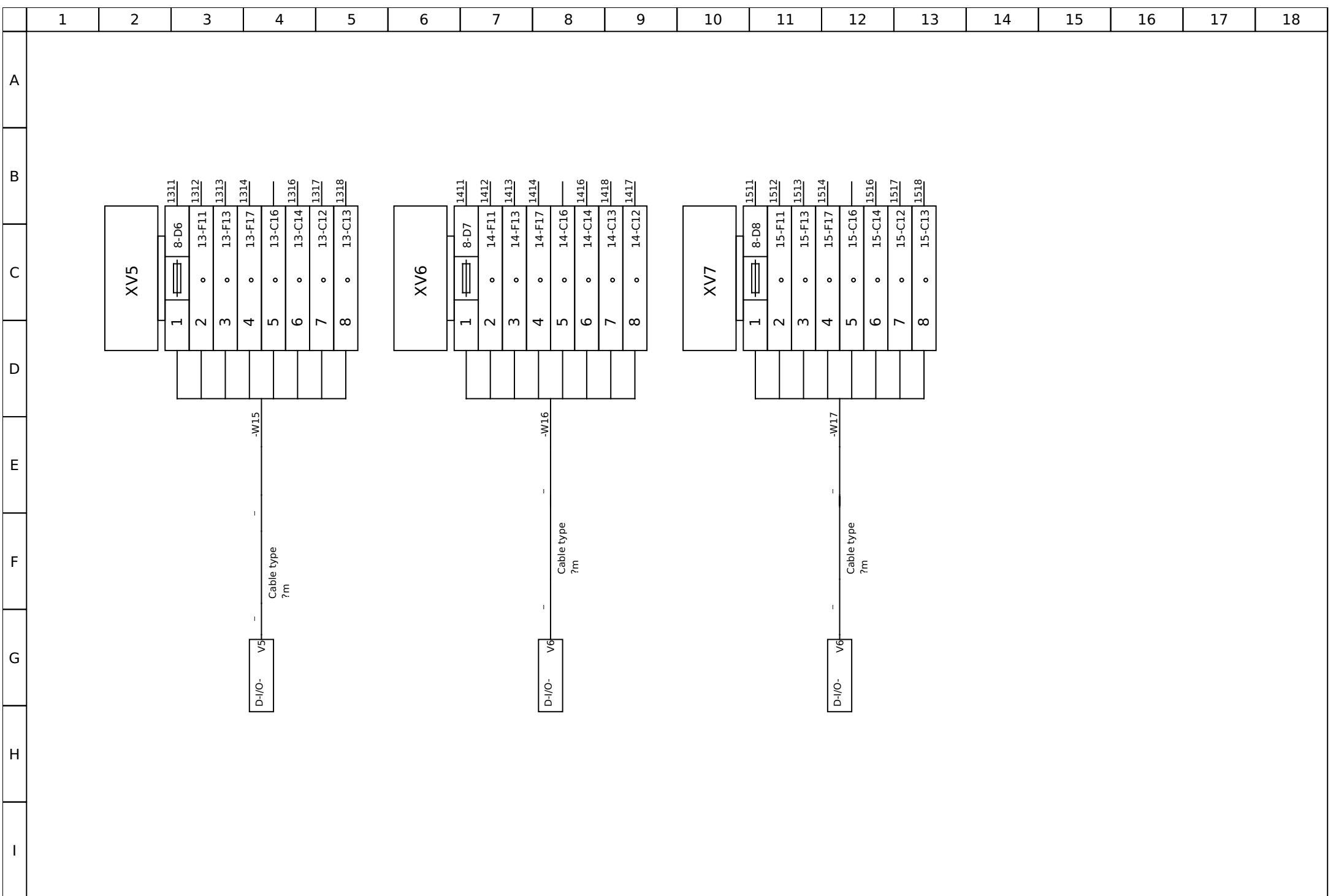


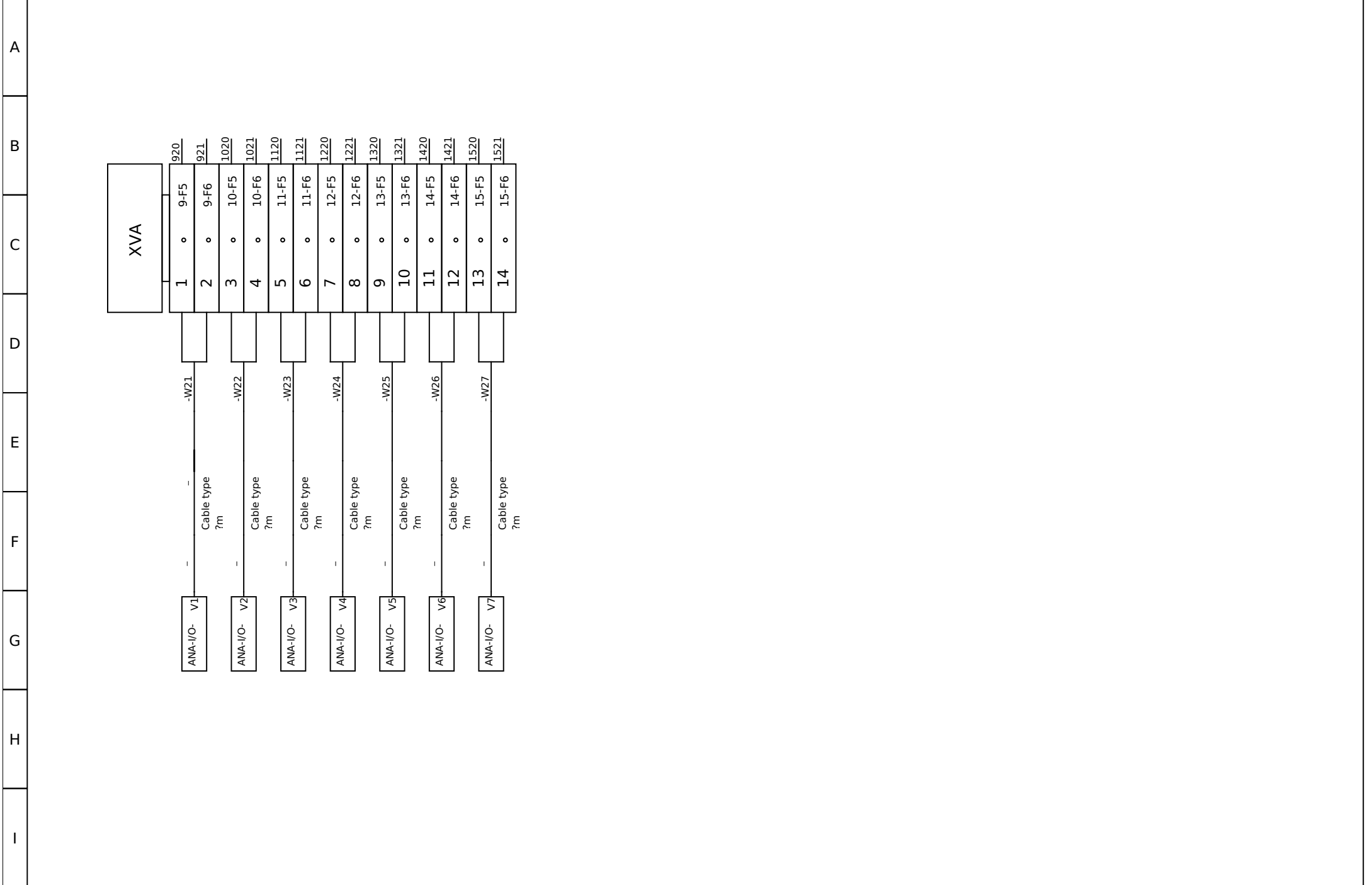
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REV		MOD		DATE		INIT		APPD AM		QET 0.7		Intake Gate Control		Electrical Cabinet		=EC1		-IGC		Next: 33	



				DATE	01/09/2018		TB1 Terminal Bord		Example project		Proj N°:	
				DSGN	IM				=EC1	-IGC		Page: 33
1	1st issue		22	CHKD	AM		QET 0.7		Intake Gate Control		Electrical Cabinet	
REV	MOD	DATE	INIT	APPD	AM						Next: 34	

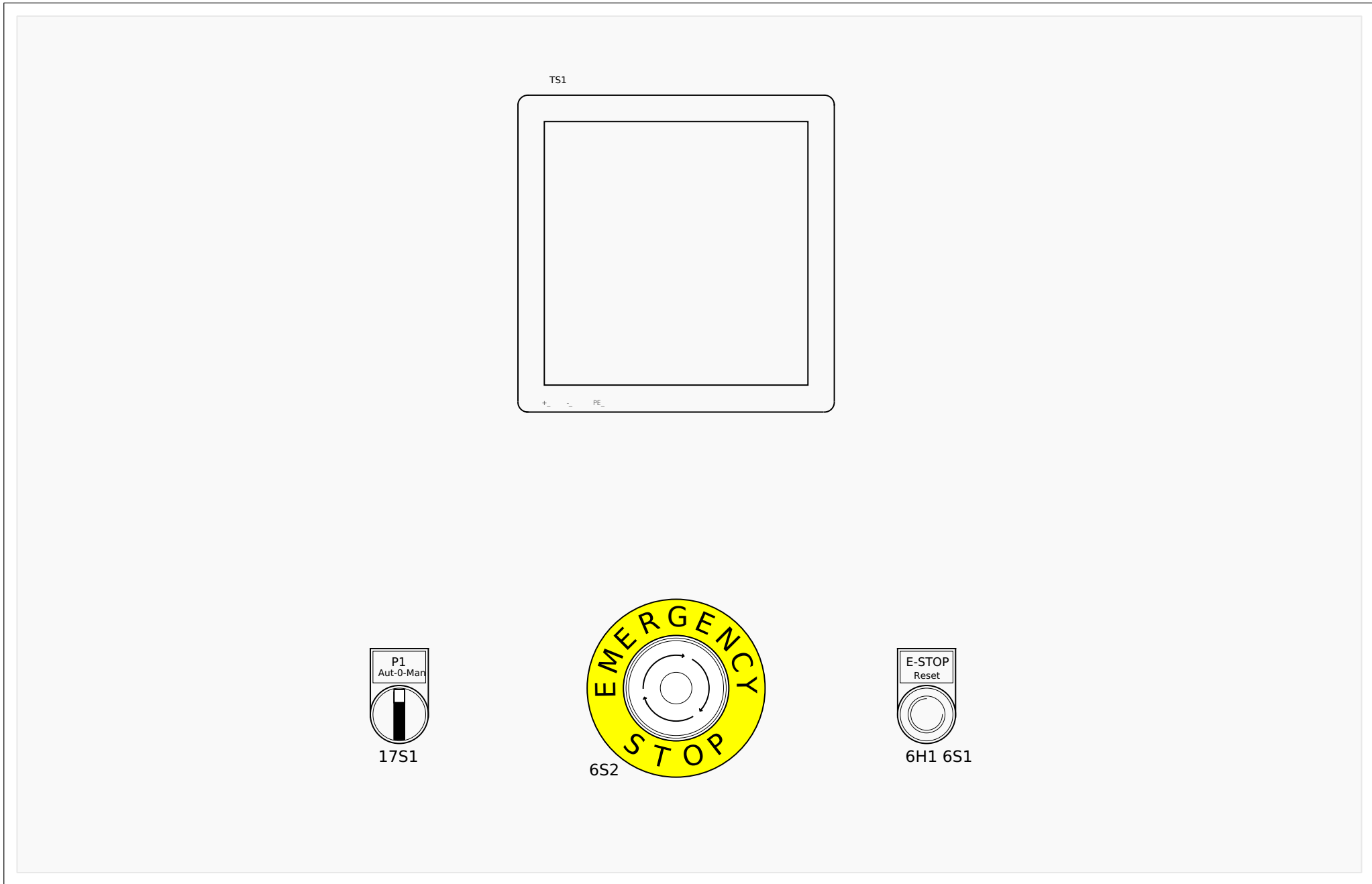


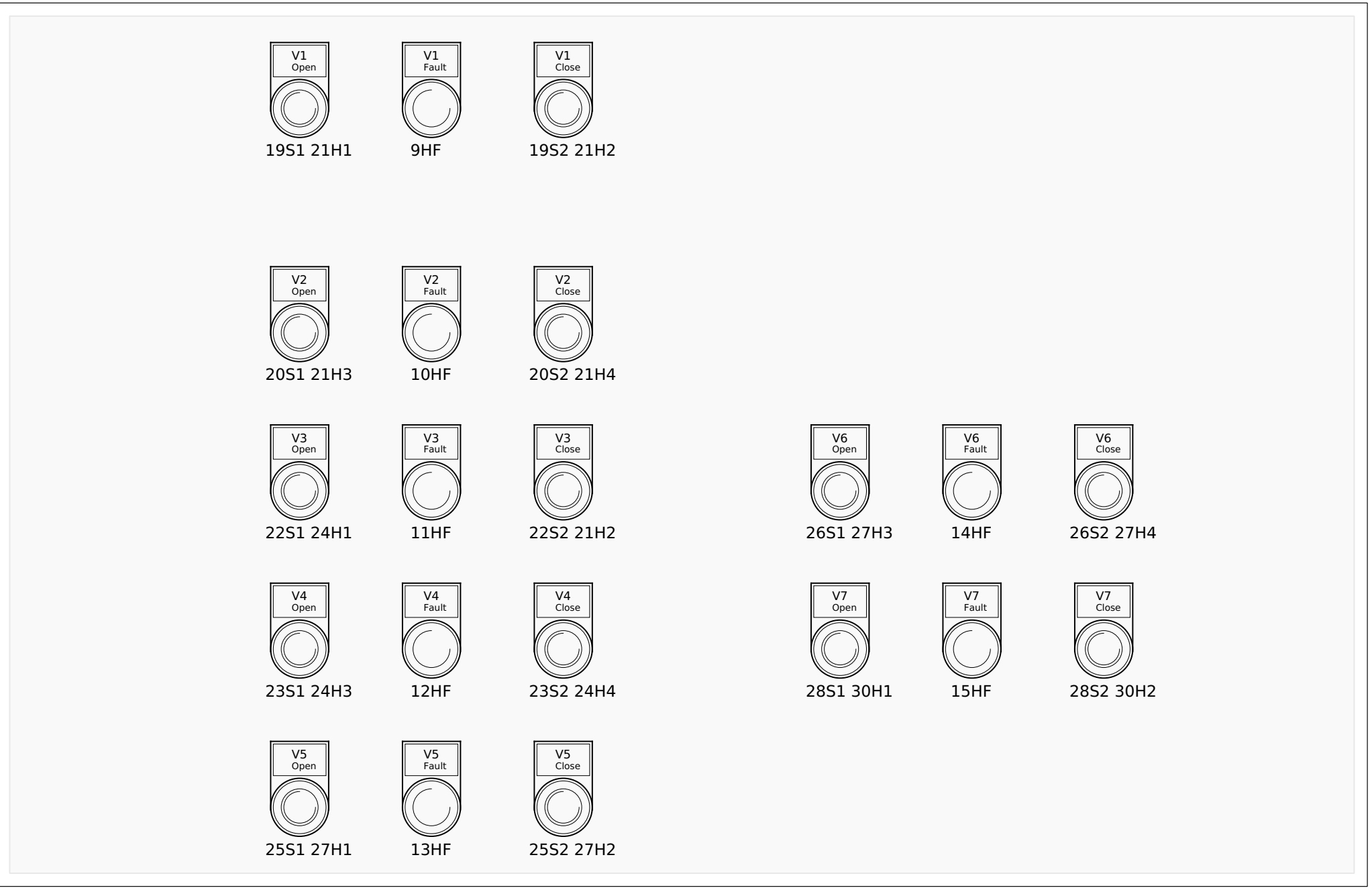




	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
A																		
B																		
C																		
D																		
E																		
F																		
G																		
H																		
I																		

				DATE	01/09/2018														Proj N°:
				DSGN	IM														
1	1st issue		22	CHKD	AM														
REV	MOD	DATE	INIT	APPD	AM	QET 0.7	Intake Gate Control	Electrical Cabinet											
										TB5 Terminal Bord			Example project				Proj N°:		
													=EC1		-IGC				Page: 37 Next: 38





A

Eth111

NETWORK settings				
IP	192	168	2	131
SUB net	255	255	255	255
Gateway	192	168	2	1

Eth113

NETWORK settings				
IP	192	168	2	133
SUB net	255	255	255	255
Gateway	192	168	2	1

B

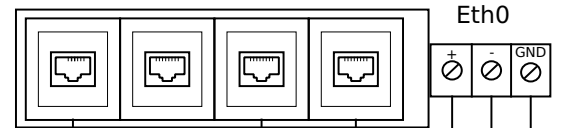
CPU-A0 Modbus TCP/IP Settings

TS Modbus TCP/IP Settings

C

16-G5

16-G1



FTP cat5e

FTP cat5e

FTP cat5e

FO Converter

F

G

H

16-H18

24V1

31-A18

0VDC

33-H18

PE

I